LB1943



Forward/Reverse Motor Driver with Braking Function

Overview

The LB1943 is a forward/reverse motor driver IC. This IC supports forward, reverse, and braking control from a single input, and the desired output voltage can be set with a resistor. Either full drive or VC drive can be selected from the single input, and the LB1943 can be controlled from a microprocessor.

Functions

- Single-input control of forward, reverse, and braking operations
- Resistor output voltage setup
- Either full drive or VC drive can be selected from the single control input.
- Can be controlled from a microprocessor.
- Built-in motor dash current absorbing device
- Built-in reference voltage circuit
- Built-in thermal protection circuit

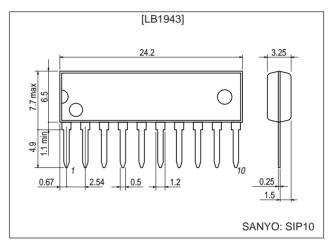
Specifications

Absolute Maximum Ratings at $Ta = 25^{\circ}C$

Package Dimensions

unit: mm

3034A-SIP10



Parameter	Symbol	Conditions	Ratings	Unit
Maximum supply voltage	V _{CC} max		18	V
Input voltage	V _{IN}	$V_{CC} \ge V_{IN}$	-0.3 to +6	V
Output current	I _{OUT}		±1.6	A
Allowable power dissipation	Pd max		1.2	W
Operating temperature	Topr		-25 to +75	°C
Storage temperature	Tstg		-55 to +125	°C

Allowable Operating Ranges at $Ta = 25^{\circ}C$

Parameter	Symbol	Conditions	Ratings	Unit
	V _{CC} 1		8 to 18	V
Supply voltage range	V _{CC} 2	$V_{CC}1 \ge V_{CC}2$	5 to 18	V
Forward-reverse disabled time	Toff		Over 20	μs

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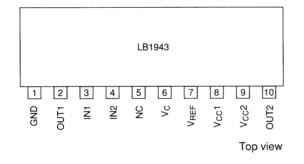
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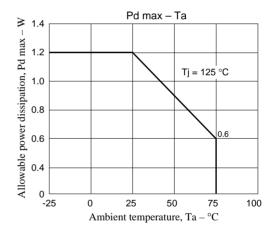
Electrical Characteristics at Ta =	$= 25^{\circ}C, V_{CC} = 12 V$
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Symbol	Conditions		Ratings			
Symbol	Conditions	min	typ	max	Unit	
V _{INL}		0		1	V	
V _{INH}		4.2		6.0	V	
V _{INM}		2		3	V	
Z _{IN}			75		kΩ	
Icc			5.5	10.0	mA	
V _{OUT} 1		4.4	4.95	5.4	V	
V _{OUT} 2		4.4	4.95	5.4	V	
I _{OL}	$R_L = \infty$		0.01	1.0	mA	
V (sat)11	V _{CC} = 12V, I _{OUT} = 300 mA		1.9	2.2	V	
V (sat)12	V _{CC} = 12V, I _{OUT} = 500 mA		1.9	2.3	V	
V (sat)21	V _{CC} = 12V, I _{OUT} = 300 mA		0.25	0.5	V	
V (sat)22	V _{CC} = 12V, I _{OUT} = 500 mA		0.4	0.65	V	
V _{REF}		6.0	6.35	6.8	V	
$\Delta V_{REF} / \Delta I_{REF}$	I _{REF} = 0 to -2.0 mA		0.05	0.1	V/mA	
	V_{OUT}/V_C , $V_C = 2.5$ V, $R_L = 60 \Omega$	1.5	1.9	2.4	×	
T _{STD}	*	150	180		°C	
	VINH VINM ZIN ICC VOUT1 VOUT2 IOL V (sat)11 V (sat)21 V (sat)22 VREF ΔVREF/ΔIREF	$\begin{tabular}{ c c c c c } \hline V_{INL} & & & & & & & & & & & & & & & & & & &$	$\begin{tabular}{ c c c c c } \hline & & & & & & & & & & & & & & & & & & $	$\begin{tabular}{ c c c c c } \hline Symbol & Conditions & \hline min & typ \\ \hline \hline W_{INL} & & 0 & & & & \\ \hline \hline V_{INH} & & 4.2 & & & \\ \hline V_{INM} & & & 2 & & & \\ \hline V_{INM} & & & 2 & & & \\ \hline I_{CC} & & & & 5.5 & & \\ \hline V_{OUT1} & R_L = 60 \ \Omega, \ V_C = 2.5 \ V & & & 4.4 & 4.95 & & \\ \hline V_{OUT2} & R_L = 60 \ \Omega, \ V_C = 2.5 \ V & & & 4.4 & 4.95 & & \\ \hline V_{OUT2} & R_L = 60 \ \Omega, \ V_C = 2.5 \ V & & & 4.4 & 4.95 & & \\ \hline V_{OUT2} & R_L = 60 \ \Omega, \ V_C = 2.5 \ V & & & 4.4 & 4.95 & & \\ \hline V_{OUT2} & R_L = 60 \ \Omega, \ V_C = 2.5 \ V & & & & 4.4 & 4.95 & & \\ \hline V_{OUT2} & R_L = 60 \ \Omega, \ V_C = 2.5 \ V & & & & & & & & \\ \hline V_{OUT2} & R_L = \infty & & & & & & & & & & \\ \hline V_{OUT2} & R_L = \infty & & & & & & & & & & & & \\ \hline V_{OUT2} & V_{CC} = 12V, \ I_{OUT} = 300 \ mA & & & & & & & & & & & & & & \\ \hline V \ (sat)11 & V_{CC} = 12V, \ I_{OUT} = 300 \ mA & & & & & & & & & & & & & & & & & & $	$\begin{tabular}{ c c c c c } \hline Symbol & Conditions & \hline min & typ & max \\ \hline min & typ & max \\ \hline 0 & 1 \\ \hline$	

Note: Items marked with an asterisk (*) are design target values, and are not tested.

Pin Assignment





Truth Table

Input		Output voltage		On continue	
IN1	IN2	OUT1	OUT2	Operation	
Н	Н	L	FULL	Forward (reverse) operation	
М	н	L	$V_{C} \times 2$	Forward (reverse) operation	
L	н	L	$V_{C} \times 2$	Forward (reverse) operation	
н	М	OFF	OFF	Braking	
М	М	OFF	OFF	Braking	
L	М	OFF	OFF	Braking	
н	L	FULL	L	Reverse (forward) operation	
М	L	$V_{C} \times 2$	L	Reverse (forward) operation	
L	L	$V_{C} \times 2$	L	Reverse (forward) operation	

Input levels: V_H: Over 4.2 V

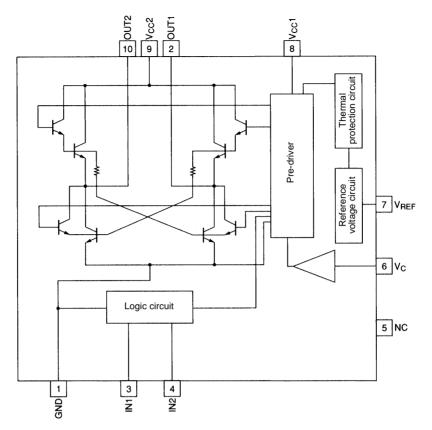
 $V_{\text{M}}: 2.0 \text{ to } 3.0 \text{ V}$ $V_{\text{L}}: \text{Under } 1.0 \text{ V}$ IN1 and IN2 go to 2.5 V when left open.

LB1943 operation is equivalent to that of the LB1641.

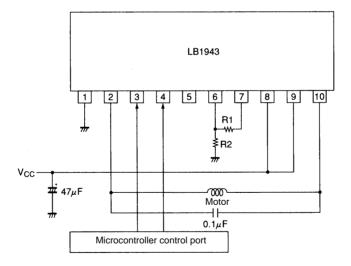
Pin Functions

Pin No.	Symbol	Pin function	Equivalent circuit
1	GND	 Power system ground. This line is shared with the signal system ground. 	
3	IN1	 Output voltage switching input Goes to V_M (about 2.5 V) when left open. 	V _{CC1} 75kΩ 75kΩ 25μA 19kΩ 19kΩ 13kΩ GND
4	IN2	 Forward, reverse, or braking control input Goes to V_M (about 2.5 V) when left open. 	V _{CC1} 75kΩ 19kΩ 19kΩ 19kΩ 13kΩ GND
6	Vc	Output voltage setting	Vcc1 6 m GND
7	V _{REF}	• Reference voltage output. V _{REF} = 6.35 V	Vcc1 (7) (7) (7) (7) (7) (7) (7) (7)
8	V _{CC} 1	Signal system power supply	
9	V _{CC} 2	Power system power supply	
2 10	OUT1 OUT2	Outputs that are connected to the motor coils	V _{CC2} 10 0ut2 10 0ut2 0ut1 0ut1 0ut1 0ut1 0ut1 0ut1

Internal Equivalent Circuit



Peripheral Circuit Example



Usage Notes

- 1. The microprocessor output ports are CMOS outputs, and must be used in the high, low, or open states.
- 2. We recommend using a value of about 60 k Ω for R1 and R2.
- 3. Voltages applied to the IN1 and IN2 pins must not exceed the range 0 to 6 V. Note that negative voltages can cause the IC to operate incorrectly. Also, do not apply voltages to IN1 or IN2 when the V_{CC} voltage is not applied.
- 4. To prevent the upper and lower output transistors from both being in the on state at the same time, when switching the IN1 and IN2 values, always hold the input open for a brief period during the transition. We recommend holding the open state for a few tens of microseconds.
- 5. A capacitor must be inserted between V_{CC} and ground. We recommend that this capacitor have a value of at least 20 μ F.

- 6. During motor drive, large currents (on the order of several hundred mA) flow in the motor power supply block. Therefore, the printed circuit board layout and interconnections must be designed so that there are no shared devices.
- 7. If negative voltages are applied to OUT1 and OUT2 and the IC operates incorrectly, insert Schottky diodes between OUT1 and ground and between OUT2 and ground.

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