TENTATIVE

SANYO Semiconductors
DATA SHEET

STK433-000N-E series

Thick-Film Hybrid IC

2-4ch class-AB Audio Power IC from 40W to 150W

Overview

The STK433-000N-E series is a hybrid IC designed to be used in from 40W to 150W x 2,3,4ch class AB audio power amplifiers.

Application

- Audio Power amplifiers

Features

- Pin-to-pin compatible outputs ranging from 40W to 150W.
- Miniature package.
- Output load impedance: RL=6Ω recommended.
- Allowable load shorted time: 0.3 second
- Allows the use of predesigned applications for standby and mute circuit.

Series model

	STK433-040N-E	STK433-060N-E	STK433-130N-E
Output1 (10%/1kHz)	40W × 2ch	50W × 2ch	150W × 2ch
Output2 (0.4%/20Hz to 20kHz)	25W × 2ch	35W × 2ch	100W × 2ch
Max. rating V_{CC} (quiescent)	±38V	±46V	±71.5V
Max. rating V_{CC} (6Ω)	±36V	±40V	±63V
Recommended operating V_{CC} (6Ω)	±24V	±27V	±44V
Dimensions (excluding pin height)	47.0mm×25.6mm×9.0mm		67.0mm×25.6mm×9.0mm

	STK433-330N-E	STK433-840N-E	STK433-890N-E
Output1 (10%/1kHz)	150W × 3ch	40W × 4ch	80W × 4ch
Output2 (0.4%/20Hz to 20kHz)	100W × 3ch	25W × 4ch	50W × 4ch
Max. rating V_{CC} (quiescent)	±71.5V	±38V	±54V
Max. rating V_{CC} (6Ω)	±63V	±36V	±47V
Recommended operating V_{CC} (6Ω)	±44V	±25V	±34V
Dimensions (excluding pin height)	64.0mm×36.6mm×9.0mm	64.0mm×31.1mm×9.0mm	78.0mm×44.1mm×9.0mm

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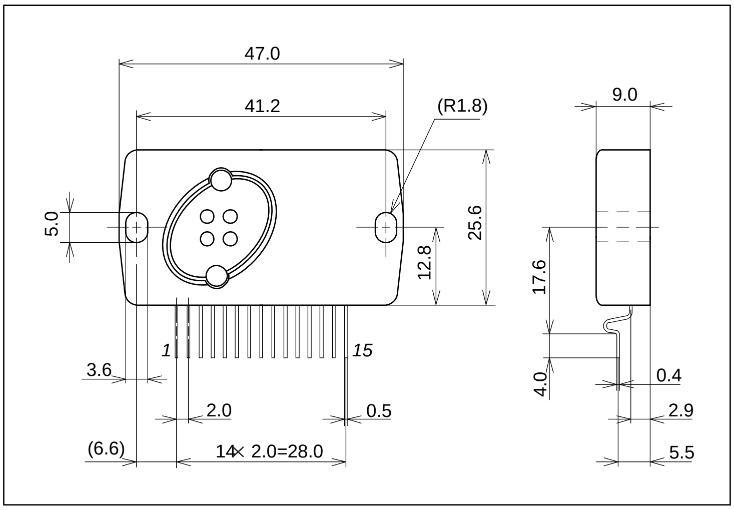
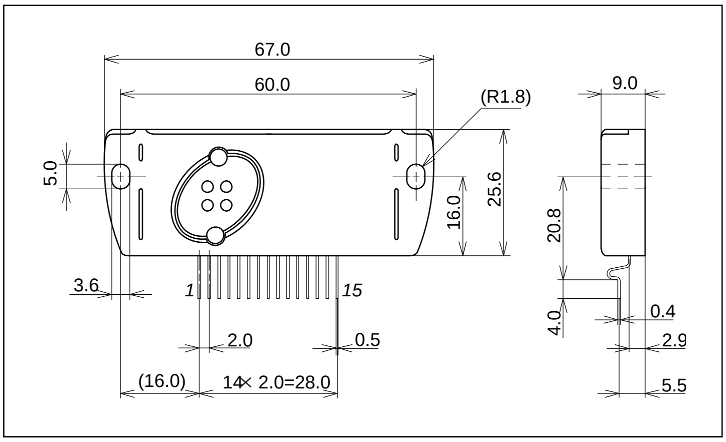
Package Dimensions (unit : mm typ)

RoHS DIRECTIVE PASS

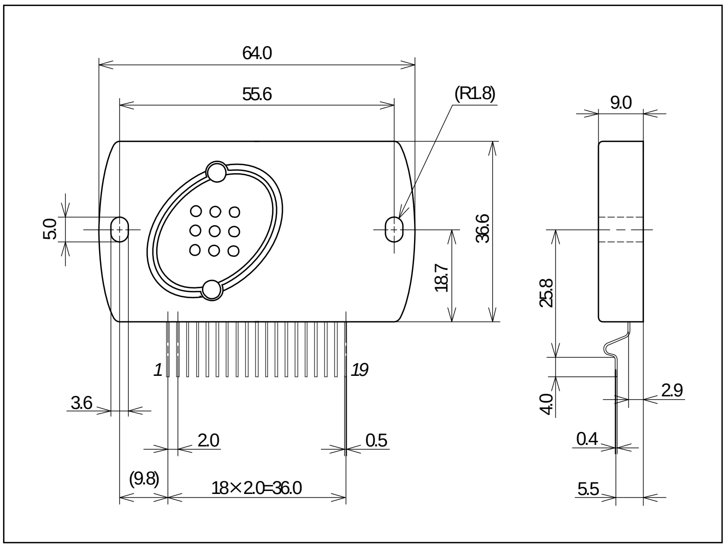
STK433-040N-E/060N-E



STK433-130N-E



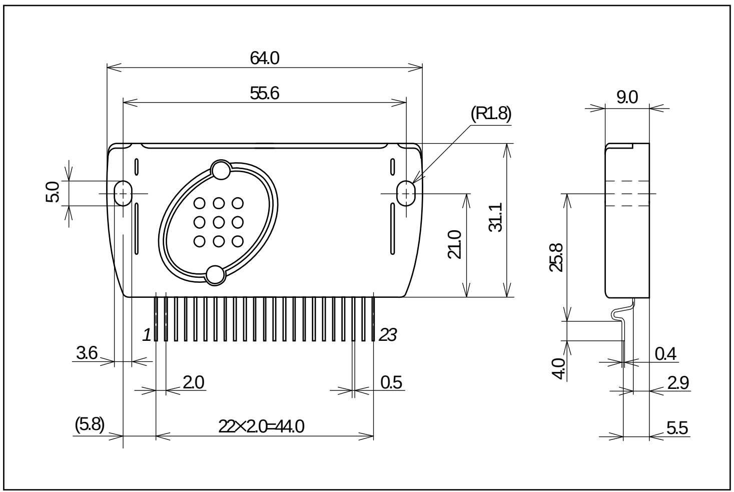
STK433-330N-E



Package Dimensions (unit : mm typ)

RoHS DIRECTIVE PASS

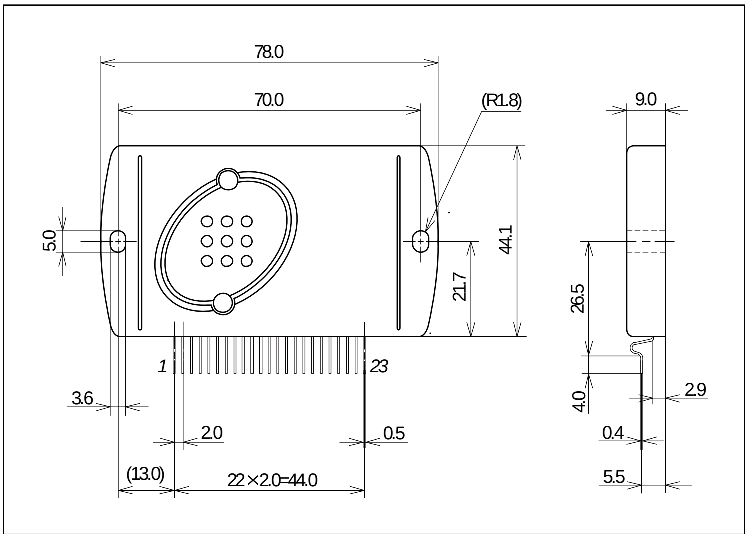
STK433-840N-E



Package Dimensions (unit : mm typ)

RoHS DIRECTIVE PASS

STK433-890N-E



Pin Layout

[STK433-000N/-100N/-300Nsr Pin Layout]

	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15
(Size) 47.0mm×25.6mm×9.0mm	2ch classAB/2.00mm														
STK433-040N 40W/JEITA STK433-060N 50W/JEITA (Size) 67.0mm×25.6mm×9.0mm STK433-130N 150W/JEITA	-PRE	-VCC	+VCC	OUT/CH1+	OUT/CH1-	OUT/CH2+	OUT/CH2-	+PRE	SUB	GND	IN/CH1	NF/CH1	STAND-BY	NF/CH2	IN/CH2

	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19
(Size) 64.0mm×36.6mm×9.0mm	3ch classAB/2.00mm																		
STK433-330N 150W/JEITA	-PRE	-VCC	+VCC	OUT/CH1+	OUT/CH1-	OUT/CH2+	OUT/CH2-	+PRE	SUB	GND	IN/CH1	NF/CH1	STAND-BY	NF/CH2	IN/CH2	IN/CH3	NF/CH3	OUT/CH3+	OUT/CH3-

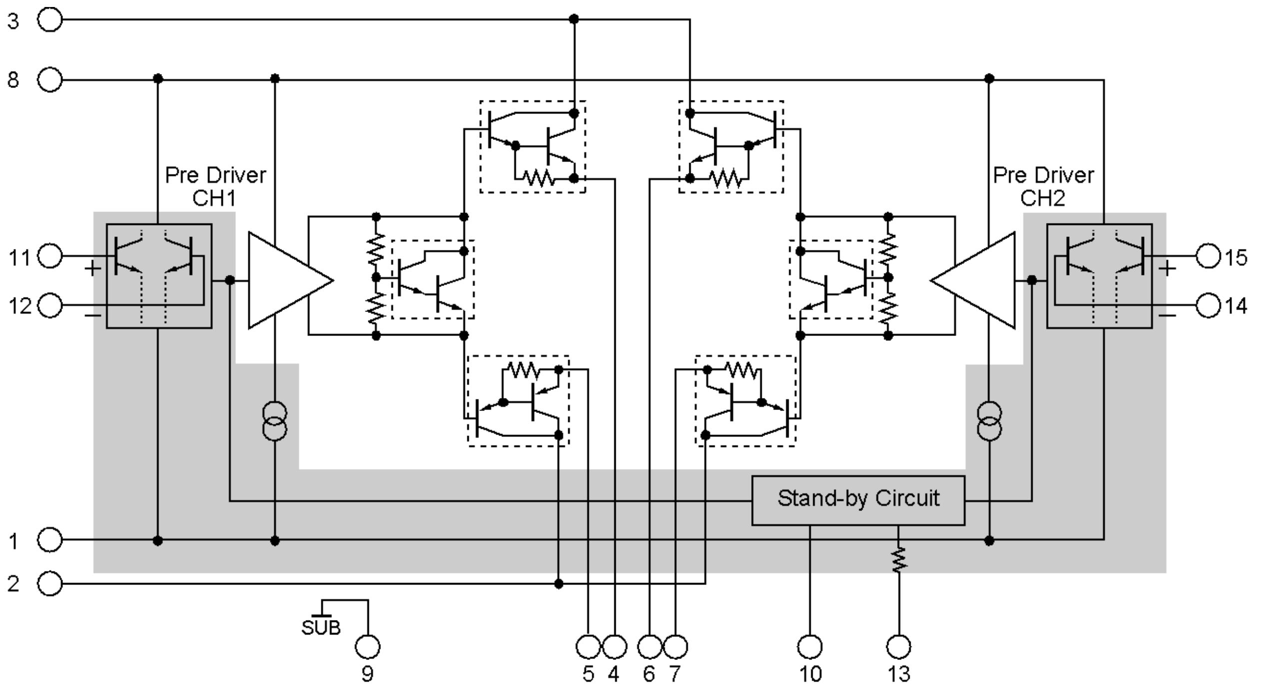
[STK433-000N/-100N/-800Nsr Pin Layout]

	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15
(Size) 47.0mm×25.6mm×9.0mm	2ch classAB/2.00mm														
STK433-040N 40W/JEITA STK433-060N 50W/JEITA (Size) 67.0mm×25.6mm×9.0mm STK433-130N 150W/JEITA	-PRE	-VCC	+VCC	OUT/CH1+	OUT/CH1-	OUT/CH2+	OUT/CH2-	+PRE	SUB	GND	IN/CH1	NF/CH1	STAND-BY	NF/CH2	IN/CH2

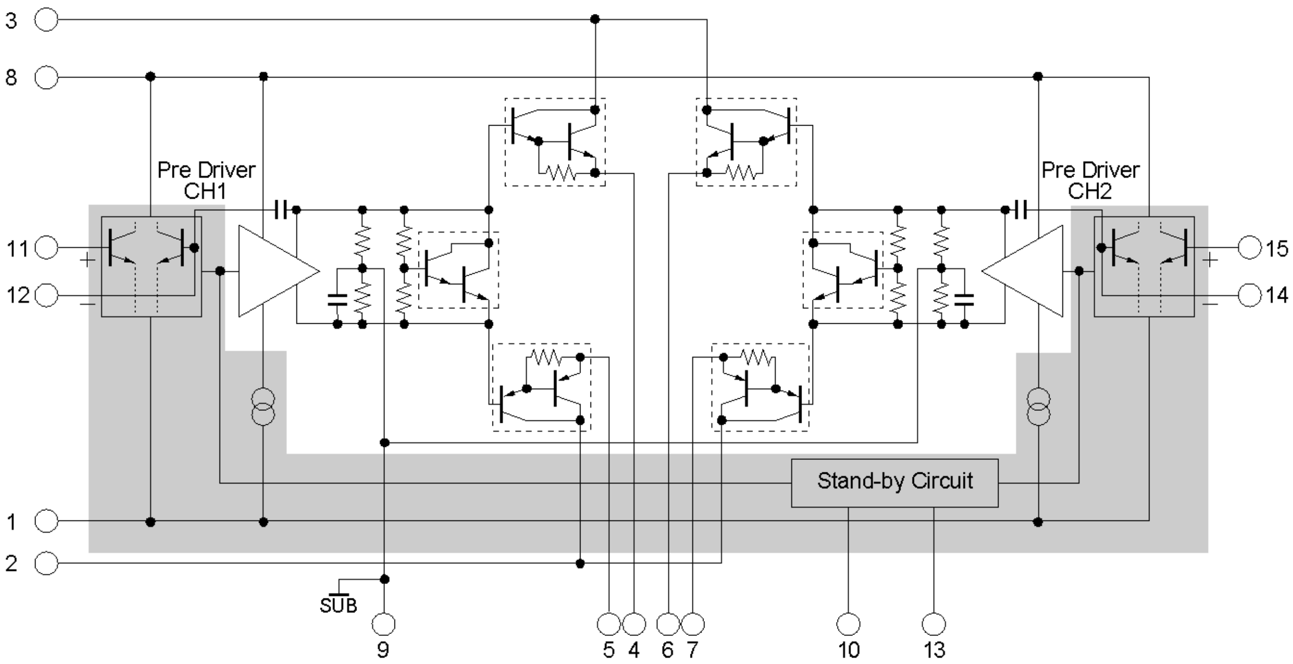
	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20	21	22	23
(Size) 64.0mm×31.1mm×9.0mm	4ch classAB/2.00mm																						
STK433-840N 40W/JEITA (Size) 78.0mm×44.1mm×9.0mm STK433-890N 80W/JEITA	-PRE	-VCC	+VCC	OUT/CH1+	OUT/CH1-	OUT/CH2+	OUT/CH2-	+PRE	SUB	GND	IN/CH1	NF/CH1	STAND-BY	NF/CH2	IN/CH2	NF/CH3	IN/CH3	IN/CH4	NF/CH4	OUT/CH3-	OUT/CH3+	OUT/CH4-	OUT/CH4+

Equivalent Circuit

STK433-040N/060N-E

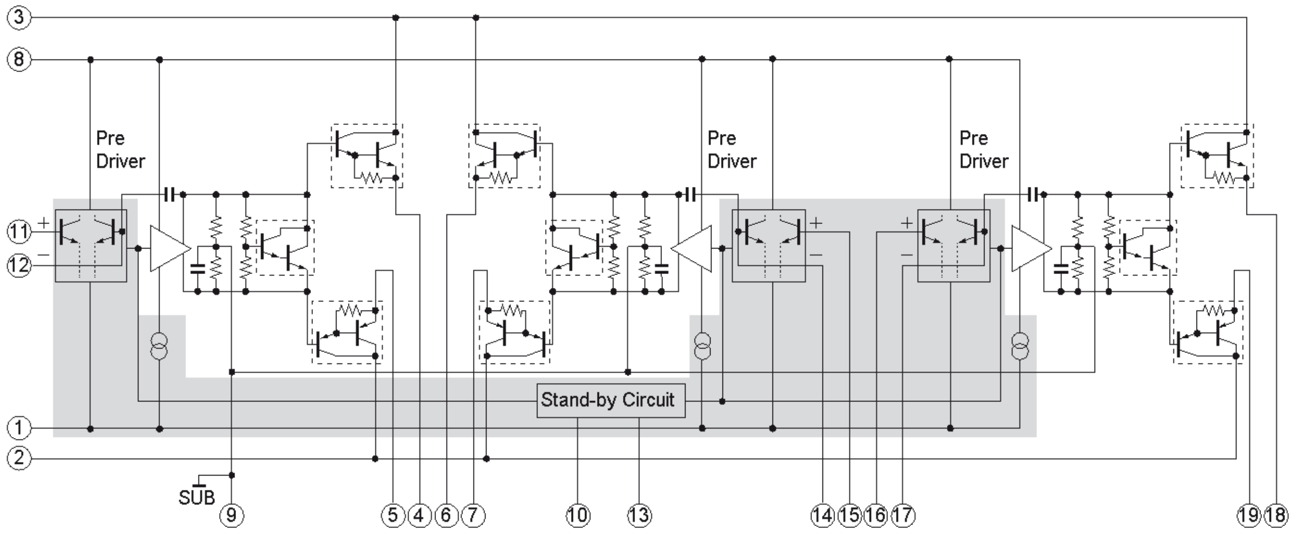


STK433-130N-E

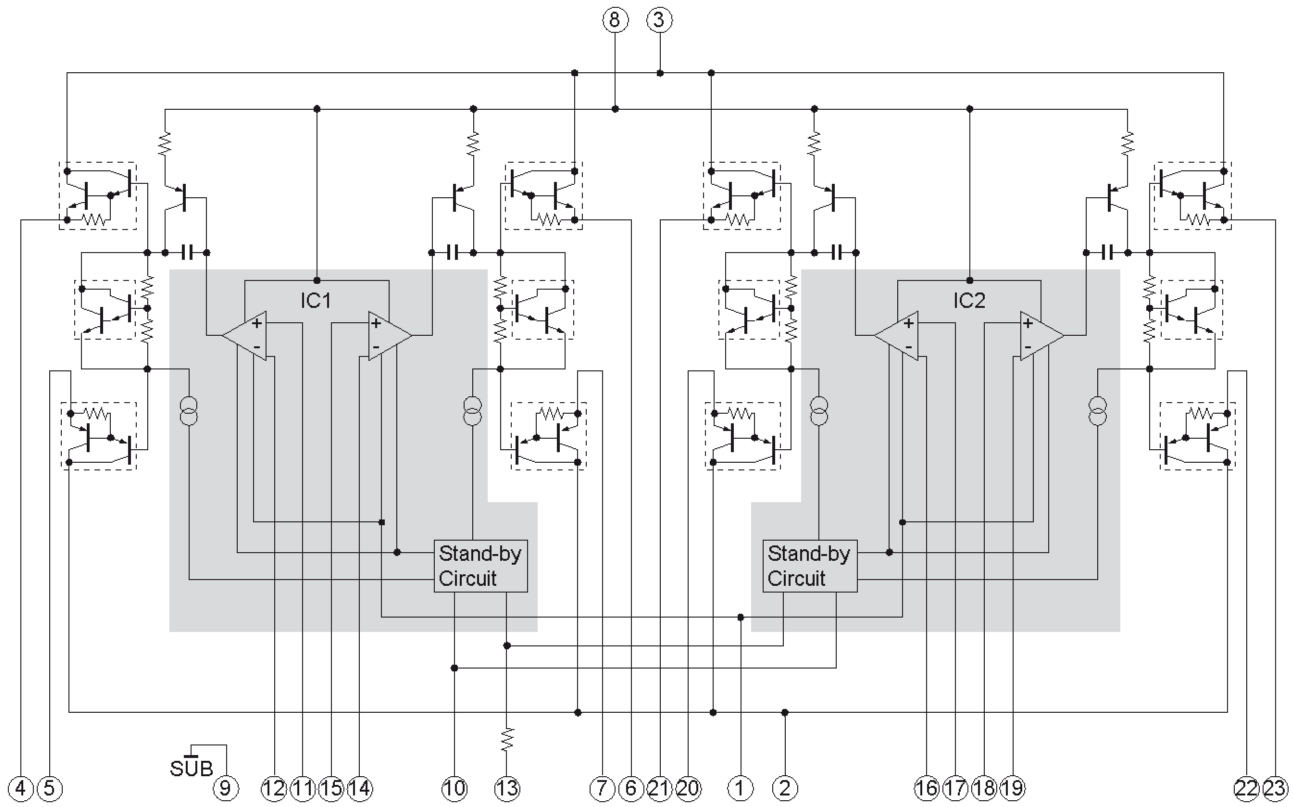


Equivalent Circuit

STK433-330N-E



STK433-840N-E/890N-E



STK433-040N-E

Specifications

Absolute maximum ratings at Ta=25°C, Tc=25°C unless otherwise specified

Parameter	Symbol	Conditions	Ratings	Unit
Maximum Power Supply Voltage	Vcc max(0)	Non- signal	±38	V
Maximum Power Supply Voltage	Vcc max(1)	Signal, RL≥6Ω	±36	V
Maximum Power Supply Voltage	Vcc max(2)	Signal, RL=4Ω	±30	V
Minimum Operation Supply Voltage	Vcc min		±10	V
#13 Operating Voltage *5	VST OFF max	#13 voltage	-0.3 to +5.5	V
Thermal Resistance	θj-c	Per one power transistor	4.2	°C/W
Junction Temperature	Tj max	Should satisfy Tj max and Tc max	150	°C
Operating Substrate Temperature	Tc max		125	°C
Storage Temperature	Tstg		-30 to +125	°C
Allowable Time for Load Short-circuit *4	ts	Vcc=±24V, RL=6Ω, f=50Hz Po=25W, 1ch drive	0.3	s

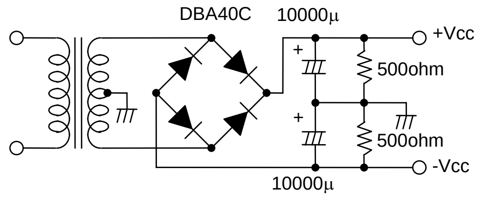
Operating Characteristics at Tc=25°C, RL=6Ω (Non-inductive Load), Rg=600Ω, VG=30dB

Parameter	Symbol	Conditions *2					Ratings			Unit
		Vcc [V]	f [Hz]	Po [W]	THD [%]		min	typ	max	
Output Power *1	Po 1	±24	20 to20k		0.4		23	25		W
	Po 2	±24	1k		10			40		
	Po 3	±20	1k		1	RL=4Ω		25		
THD *1	THD 1	±24	20 to20k	5.0		VG=30dB			0.4	%
	THD 2	±24	1k					0.02		
Frequency Characteristics *1	fL, fH	±24		1.0		+0 -3dB		20 to 50k		Hz
Input Impedance	ri	±24	1k	1.0				55		kΩ
Output Noise Voltage *3	VNO	±29				Rg=2.2kΩ			1.0	mVrms
Quiescent Current	Icco	±29				No load	15	30	70	mA
Quiescent Current at Stand-by	ICST	±29							1.0	mA
Output Neutral Voltage	VN	±29					-70	0	+70	mV
#13 Stand-By ON Threshold *5	VST ON	±24				Stand-by		0	0.6	V
#13 Stand-By OFF Threshold *5	VST OFF	±24				Operation	2.5	3.0	5.5	V

Note

*1. 1 channel Operation.
*2. All tests are measured using a constant-voltage supply unless otherwise specified
*3. The output noise voltage is peak value of an average-reading meter with a rms value scale (VTVM).
A regulated AC supply (50Hz) should be used to eliminate the effects of AC primary line flicker noise
*4. Allowable time for load short-circuit and output noise voltage are measured using the specified transformer power supply.
*5. Please connect - PreVcc pin (#1 pin) with the stable minimum voltage.
and connect so that current does not flow in by reverse bias.
*6. In case of heat sink design, we request customer to design in the condition to have assumed market.
* The case of this Hybrid-IC is using thermosetting silicon adhesive(TSE322SX).
* Weight of HIC : (typ) 12.0 g
Outer carton dimensions (W×L×H) : 452mm×325mm×192mm

Specified Transformer Power Supply
(Equivalent to MG-200)



STK433-060N-E

Specifications

Absolute maximum ratings at Ta=25°C, Tc=25°C unless otherwise specified

Parameter	Symbol	Conditions	Ratings	Unit
Maximum Power Supply Voltage	Vcc max(0)	Non- signal	±46	V
Maximum Power Supply Voltage	Vcc max(1)	Signal, RL≥6Ω	±40	V
Maximum Power Supply Voltage	Vcc max(2)	Signal, RL=4Ω	±33	V
Minimum Operation Supply Voltage	Vcc min		±10	V
#13 Operating Voltage *5	VST OFF max	#13 voltage	-0.3 to +5.5	V
Thermal Resistance	θj-c	Per one power transistor	3.5	°C/W
Junction Temperature	Tj max	Should satisfy Tj max and Tc max	150	°C
Operating Substrate Temperature	Tc max		125	°C
Storage Temperature	Tstg		-30 to +125	°C
Allowable Time for Load Short-circuit *4	ts	Vcc=±27V, RL=6Ω, f=50Hz Po=35W, 1ch drive	0.3	s

Operating Characteristics at Tc=25°C, RL=6Ω(Non-inductive Load), Rg=600Ω, VG=30dB

Parameter	Symbol	Conditions *2					Ratings			Unit
		Vcc [V]	f [Hz]	Po [W]	THD [%]		min	typ	max	
Output Power *1	Po 1	±27	20 to20k		0.4		33	35		W
	Po 2	±27	1k		10			50		
	Po 3	±22	1k		1	RL=4Ω		35		
THD *1	THD 1	±27	20 to20k	5.0		VG=30dB			0.4	%
	THD 2	±27	1k					0.02		
Frequency Characteristics *1	fL, fH	±27		1.0		+0 -3dB	20 to 50k			Hz
Input Impedance	ri	±27	1k	1.0				55		kΩ
Output Noise Voltage *3	VNO	±33				Rg=2.2kΩ			1.0	mVrms
Quiescent Current	Icco	±33				No load	15	30	70	mA
Quiescent Current at Stand-by	ICST	±33				VST=0v			1.0	mA
Output Neutral Voltage	VN	±33					-70	0	+70	mV
#13 Stand-By ON Threshold *5	VST ON	±27				Stand-by		0	0.6	V
#13 Stand-By OFF Threshold *5	VST OFF	±27				Operation	2.5	3.0	5.5	V

Note

*1. 1 channel Operation.

*2. All tests are measured using a constant-voltage supply unless otherwise specified

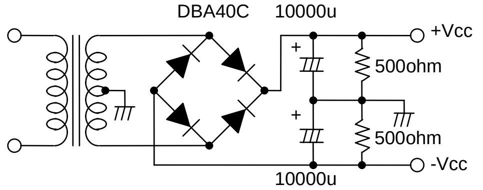
*3. The output noise voltage is peak value of an average-reading meter with a rms value scale (VTVM). A regulated AC supply (50Hz) should be used to eliminate the effects of AC primary line flicker noise

*4. Allowable time for load short-circuit and output noise voltage are measured using the specified transformer power supply.

*5. The impression voltage of '#13(Stand-By) pin' must not exceed the maximum rating. Power amplifier operate by impressing voltage +2.5 to +5.5v to '#13(Stand-By) pin'.

* Please connect - PreVcc pin (#1 pin) with the stable minimum voltage. and connect so that current does not flow in by reverse bias.
* In case of heat sink design, we request customer to design in the condition to have assumed market.
* The case of this Hybrid-IC is using thermosetting silicon adhesive(TSE322SX).
* Weight of HIC : (typ) 12.0 g
  Outer carton dimensions (W×L×H) : 452mm×325mm×192mm

Specified Transformer Power Supply
(Equivalent to MG-200)



STK433-130N-E

Specifications

Absolute maximum ratings at Ta=25°C, Tc=25°C unless otherwise specified

Parameter	Symbol	Conditions	Ratings	Unit
Maximum Power Supply Voltage	Vcc max(0)	Non- signal	±71.5	V
Maximum Power Supply Voltage	Vcc max(1)	Signal, RL≥6Ω	±63	V
Maximum Operation Supply Voltage	Vcc min		±10	V
#13 Operating Voltage *5	VST OFF max		-0.3 to +5.5	V
Thermal Resistance	θj-c	Per one power transistor	1.6	°C/W
Junction Temperature	Tj max	Should satisfy Tj max and Tc max	150	°C
Operating Substrate Temperature	Tc max		125	°C
Storage Temperature	Tstg		-30 to +125	°C
Allowable Time for Load Short-circuit *4	ts	Vcc=±44V,RL=6Ω,f=50Hz Po=100W,1ch drive	0.3	s

Operating Characteristics at Tc=25°C, RL=6Ω(Non-inductive Load), Rg=600Ω, VG=30dB

Parameter	Symbol	Conditions					Ratings			Unit
		Vcc [V]	f [Hz]	Po [W]	THD [W]		min	typ	max	
Output Power *1	Po 1	±44	20 to20k		0.4		96	100		W
	Po 2	±44	1k		10			150		
THD *1	THD 1	±44	20 to20k	5.0		VG=30dB			0.4	%
	THD 2	±44	1k					0.01		
Frequency Characteristics *1	fL, fH	±44		1.0		+0 -3dB	20 to 50k			Hz
Input Impedance	ri	±44	1k	1.0				55		kΩ
Output Noise Voltage *3	VNO	±53				Rg=2.2kΩ			1.0	mVrms
Quiescent Current	Icco	±53				No load	40	80	100	mA
Output Neutral Voltage	VN	±53					-70	0	+70	mV
#13 Stand-By ON Threshold *5	VST ON	±44				Stand-by		0	0.6	V
#13 Stand-By OFF Threshold *5	VST OFF	±44				Operation	2.5	3.0	5.5	V

Note

*1.1channel Operation.

*2.All tests are measured using a constant-voltage supply unless otherwise specified

*3.The output noise voltage is peak value of an average-reading meter with a rms value scale (VTVM). A regulated AC supply (50Hz) should be used to eliminate the effects of AC primary line flicker noise

*4.Allowable time for load short-circuit and output noise voltage are measured using the specified transformer power supply.

*5. The impression voltage of '#13(Stand-By) pin' must not exceed the maximum rating. Power amplifier operate by impressing voltage +2.5 to +5.5v to '#13(Stand-By) pin'.

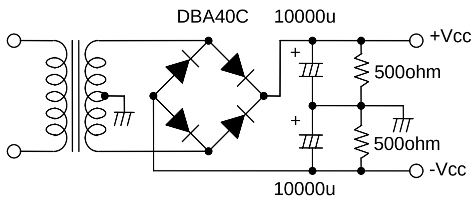
* Please connect - PreVcc pin (#1 pin)with the stable minimum voltage. and connect so that current does not flow in by reverse bias.

* In case of heat sink design, we request customer to design in the condition to have assumed market.

* The case of this Hybrid-IC is using thermosetting silicon adhesive(TSE322SX).

* Weight of HIC : (typ)18.4 g
 Outer carton dimensions (W×L×H) : 429mm×245mm×275mm

Specified Transformer Power Supply
(Equivalent to MG-250)



STK433-330N-E

Specifications

Absolute maximum ratings at Ta=25°C, Tc=25°C unless otherwise specified

Parameter	Symbol	Conditions	Ratings	Unit
Maximum Power Supply Voltage	Vcc max(0)	Non- signal	±71.5	V
Maximum Power Supply Voltage	Vcc max(1)	Signal, RL≥6Ω	±63	V
Maximum Operation Supply Voltage	Vcc min		±10	V
#13 Operating Voltage *5	VST OFF max		-0.3 to +5.5	V
Thermal Resistance	θj-c	Per one power transistor	1.6	°C/W
Junction Temperature	Tj max	Should satisfy Tj max and Tc max	150	°C
Operating Substrate Temperature	Tc max		125	°C
Storage Temperature	Tstg		-30 to +125	°C
Allowable Time for Load Short-circuit *4	ts	Vcc=±44V,RL=6Ω,f=50Hz Po=100W,1ch drive	0.3	s

Operating Characteristics at Tc=25°C, RL=6Ω(Non-inductive Load), Rg=600Ω, VG=30dB

Parameter	Symbol	Conditions					Ratings			Unit
		Vcc [V]	f [Hz]	Po [W]	THD [W]		min	typ	max	
Output Power *1	Po 1	±44	20 to20k		0.4		96	100		W
	Po 2	±44	1k		10			150		
THD *1	THD 1	±44	20 to20k	5.0		VG=30dB			0.4	%
	THD 2	±44	1k					0.01		
Frequency Characteristics *1	fL, fH	±44		1.0		+0 -3dB		20 to 50k		Hz
Input Impedance	ri	±44	1k	1.0				55		kΩ
Output Noise Voltage *3	VNO	±53				Rg=2.2kΩ			1.0	mVrms
Quiescent Current	Icco	±53				No load	60	120	160	mA
Output Neutral Voltage	VN	±53					-70	0	+70	mV
#13 Stand-By ON Threshold *5	VST ON	±44				Stand-by		0	0.6	V
#13 Stand-By OFF Threshold *5	VST OFF	±44				Operation	2.5	3.0	5.5	V

Note

*1.1channel Operation.

*2.All tests are measured using a constant-voltage supply unless otherwise specified

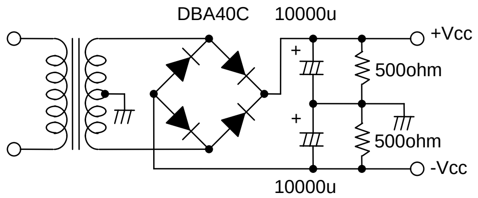
*3.The output noise voltage is peak value of an average-reading meter with a rms value scale (VTVM). A regulated AC supply (50Hz) should be used to eliminate the effects of AC primary line flicker noise

*4.Allowable time for load short-circuit and output noise voltage are measured using the specified transformer power supply.

*5. The impression voltage of '#13(Stand-By)pin' must not exceed the maximum rating. Power amplifier operate by impressing voltage +2.5 to +5.5v to '#13(Stand-By)pin'.

* Please connect - PreVcc pin (#1 pin)with the stable minimum voltage. and connect so that current does not flow in by reverse bias.
* In case of heat sink design, we request customer to design in the condition to have assumed market.
* The case of this Hybrid-IC is using thermosetting silicon adhesive(TSE322SX).
* Weight of HIC : (typ)24.5g
  Outer carton dimensions (W×L×H) : 452mm×325mm×192mm

Specified Transformer Power Supply
(Equivalent to MG-250)



STK433-840N-E

Specifications

Absolute maximum ratings at Ta=25°C, Tc=25°C unless otherwise specified

Parameter	Symbol	Conditions	Ratings	Unit
Maximum supply voltage	Vcc max(0)	No signal	±38	V
	Vcc max(1)	Signal, RL≥6Ω	±36	V
	Vcc max(2)	Signal, RL=4Ω	±30	V
Minimum supply voltage	Vcc min		±10	V
#13pin Operating Voltage *5	VST OFF max	#13pin voltage	-0.3 to +5.5	V
Thermal resistance	θj-c	Per power transistor	4.2	°C/W
Junction temperature	Tj max	Both the Tj max and Tc max conditions must be met.	150	°C
Operating Substrate Temperature	Tc max		125	°C
Storage temperature	Tstg		-30 to +125	°C
Allowable load shorted time *4	ts	Vcc=±25V,RL=6Ω,f=50Hz Po=25W,1ch Drive	0.3	s

Operating Characteristics at Tc=25°C, RL=6Ω(Non-inductive Load), Rg=600Ω, VG=30dB

Parameter	Symbol	Conditions *2					Ratings			Unit
		Vcc [V]	f [Hz]	Po [W]	THD [%]		min	typ	max	
Output power *1	Po 1	±25	20 to 20k		0.6		23	25		W
	Po 2	±25	1k		10			40		
THD *1	THD 1	±25	20 to 20k	5.0		VG=30dB			0.6	%
	THD 2	±25	1k					0.02		
Frequency characteristics *1	fL, fH	±25		1.0		+0 -3dB	20 to 50k			Hz
Input impedance	Ri	±25	1k	1.0				55		kΩ
Output noise voltage *3	VNO	±30				Rg=2.2 kΩ			1.0	mVrms
Quiescent current	Icco	±30				No load	30	60	140	mA
Quiescent Current at Stand-by	ICST	±30				VST=0v			1.0	mA
Neutral voltage	VN	±30					-70	0	+70	mV
#13 Stand-By ON Threshold *5	VST ON	±25				Stand-by		0	0.6	V
#13 Stand-By OFF Threshold *5	VST OFF	±25				Operation	2.5	3.0	5.5	V

[Note]

*1. 1channel Operation.

*2. All tests are measured using a constant-voltage supply unless otherwise specified

*3. The output noise voltage is peak value of an average-reading meter with a rms value scale (VTVM). A regulated AC supply (50Hz) should be used to eliminate the effects of AC primary line flicker noise

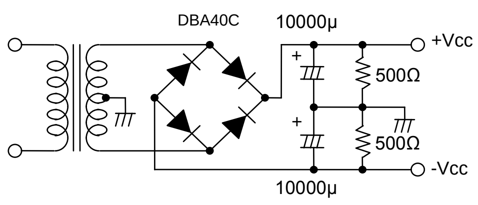
*4. Allowable time for load short-circuit and output noise voltage are measured using the specified transformer power supply.

*5. The impression voltage of '#13(Stand-By)pin' must not exceed the maximum rating. Power amplifier operate by impressing voltage +2.5 to +5.5V to '#13(Stand-By) pin'.

* Please connect - PreVcc pin (#1 pin)with the stable minimum voltage and connect so that current does not flow in by reverse bias.
* In case of heat sink design, we request customer to design in the condition to have assumed market.
* The case of this Hybrid-IC is using thermosetting silicon adhesive(TSE322SX).
* Weight of 1 HIC : (typ) 20.6 g
  Outer carton dimensions (W×L×H) : 502mm×247mm×282mm

Specified Transformer Power Supply

(Equivalent to MG-200)



STK433-890N-E

Specifications

Absolute maximum ratings at Ta=25°C, Tc=25°C unless otherwise specified

Parameter	Symbol	Conditions	Ratings	Unit
Maximum supply voltage	Vcc max(0)	No signal	±54	V
	Vcc max(1)	Signal, RL≥6Ω	±47	V
	Vcc max(2)	Signal, RL=4Ω	±40	V
Minimum supply voltage	Vcc min		±10	V
#13pin Operating Voltage *5	VST OFF max	#13pin voltage	-0.3 to +5.5	V
Thermal resistance	θj-c	Per power transistor	2.1	°C/W
Junction temperature	Tj max	Both the Tj max and Tc max conditions must be met.	150	°C
Operating Substrate Temperature	Tc max		125	°C
Storage temperature	Tstg		-30 to +125	°C
Allowable load shorted time *4	ts	Vcc=±34V,RL=6Ω,f=50Hz Po=50W,1ch Drive	0.3	s

Operating Characteristics at Tc=25°C, RL=6Ω(Non-inductive Load), Rg=600Ω, VG=30dB

Parameter	Symbol	Conditions *2					Ratings			Unit
		Vcc [V]	f [Hz]	Po [W]	THD [%]		min	typ	max	
Output power *1	Po 1	±34	20 to 20k		0.6		47	50		W
	Po 2	±34	1k		10			80		
THD *1	THD 1	±34	20 to 20k	5.0		VG=30dB			0.6	%
	THD 2	±34	1k					0.02		
Frequency characteristics *1	fL, fH	±34		1.0		+0 -3dB	20 to 50k			Hz
Input impedance	ri	±34	1k	1.0				55		kΩ
Output noise voltage *3	VNO	±40				Rg=2.2 kΩ			1.0	mVrms
Quiescent current	Icco	±40				No load	90	150	210	mA
Quiescent Current at Stand-by	ICST	±40				VST=0v			1.0	mA
Neutral voltage	VN	±40					-70	0	+70	mV
#13 Stand-By ON Threshold *5	VST ON	±34				Stand-by		0	0.6	V
#13 Stand-By OFF Threshold *5	VST OFF	±34				Operation	2.5	3.0	5.5	V

[Note]

*1. 1channel Operation.

*2. All tests are measured using a constant-voltage supply unless otherwise specified

*3. The output noise voltage is peak value of an average-reading meter with a rms value scale (VTVM). A regulated AC supply (50Hz) should be used to eliminate the effects of AC primary line flicker noise

*4. Allowable time for load short-circuit and output noise voltage are measured using the specified transformer power supply.

*5. The impression voltage of '#13(Stand-By)pin' must not exceed the maximum rating. Power amplifier operate by impressing voltage +2.5 to +5.5V to '#13(Stand-By) pin'.

* Please connect : PreVcc pin (#1 pin)with the stable minimum voltage and connect so that current does not flow in by reverse bias.

* In case of heat sink design, we request customer to design in the condition to have assumed market.

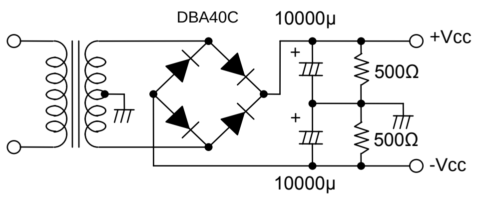
* The case of this Hybrid-IC is using thermosetting silicon adhesive(TSE322SX).

* Weight of 1 HIC : (typ) 37.0 g

Outer carton dimensions (W×L×H) : 452mm×325mm×192mm

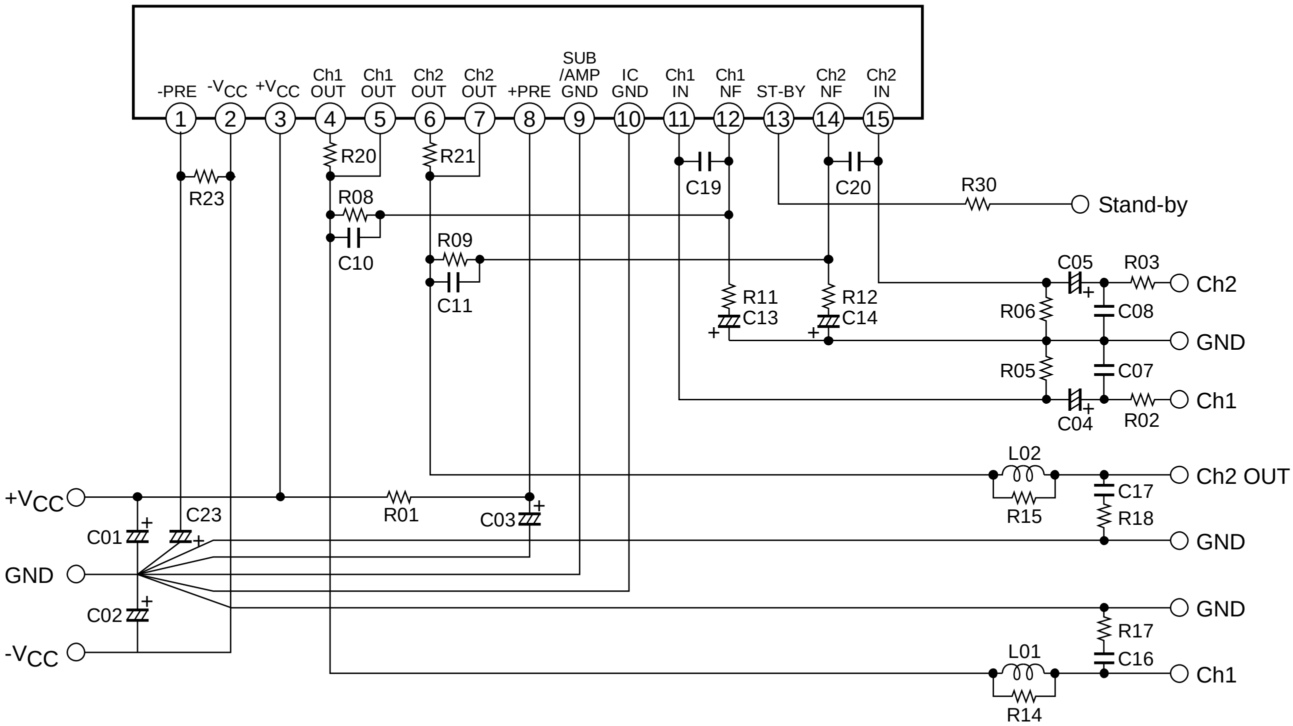
Specified Transformer Power Supply

(Equivalent to MG-200)

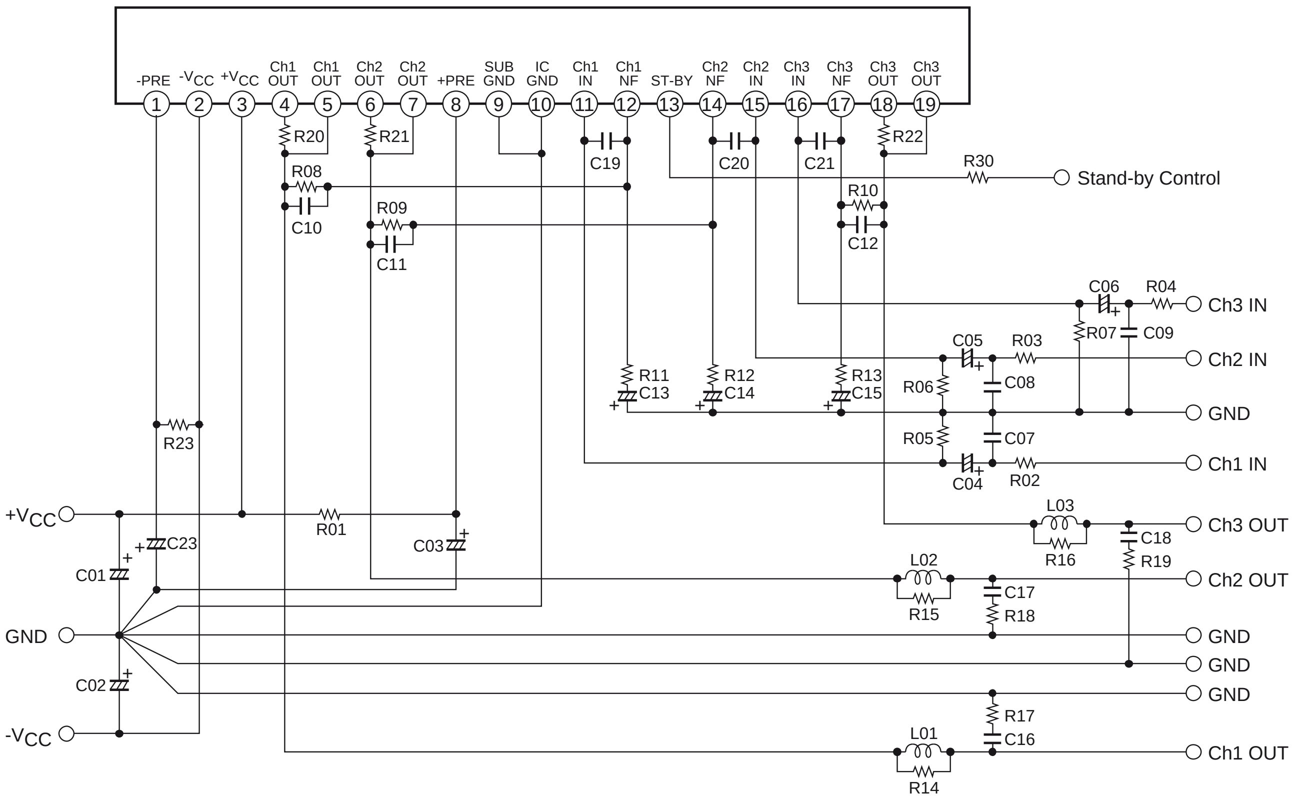


Application Circuit

STK433-040N-E/060N-E/130N-E

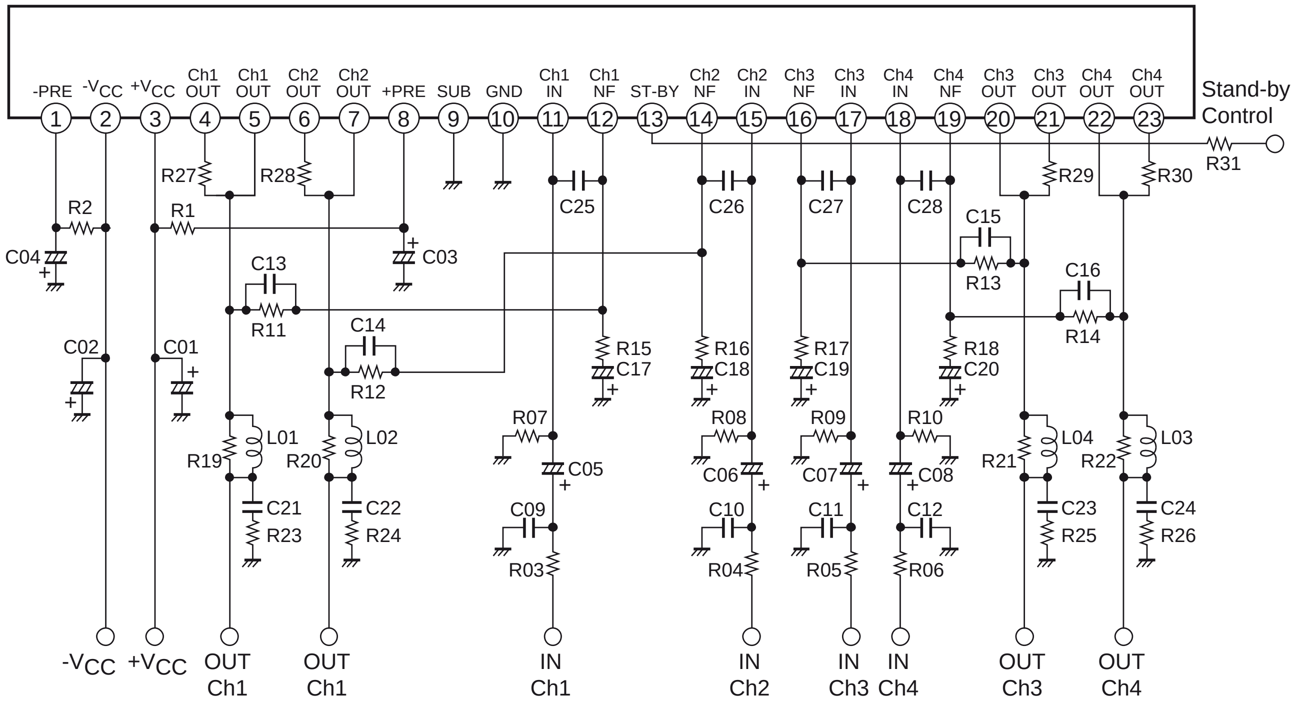


STK433-330N-E



Application Circuit

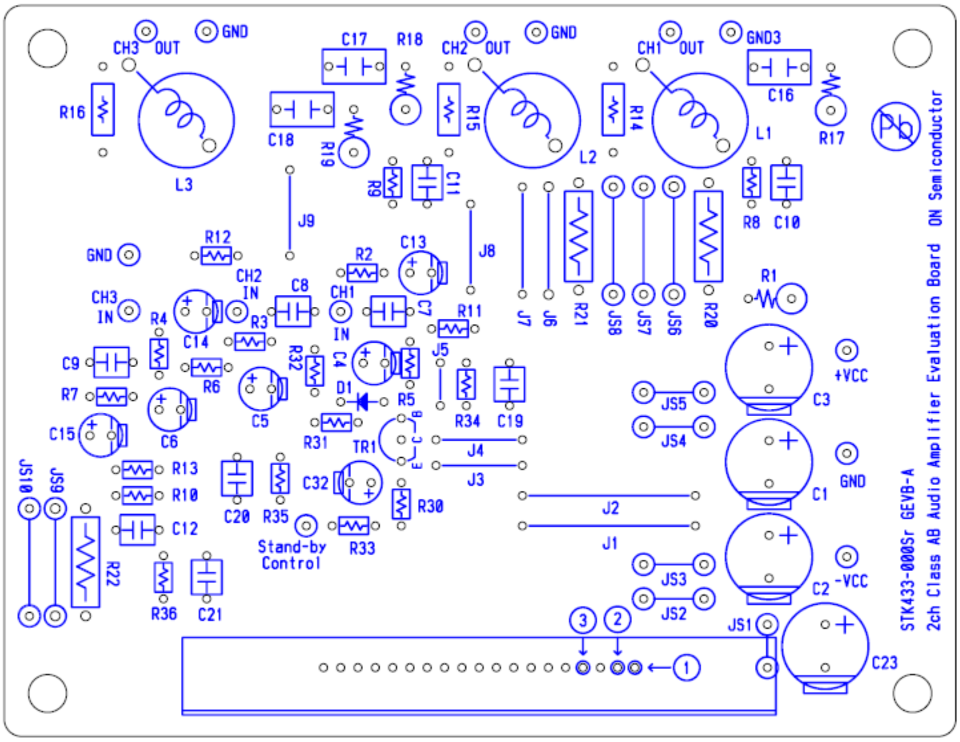
STK433-840N-E/890N-E

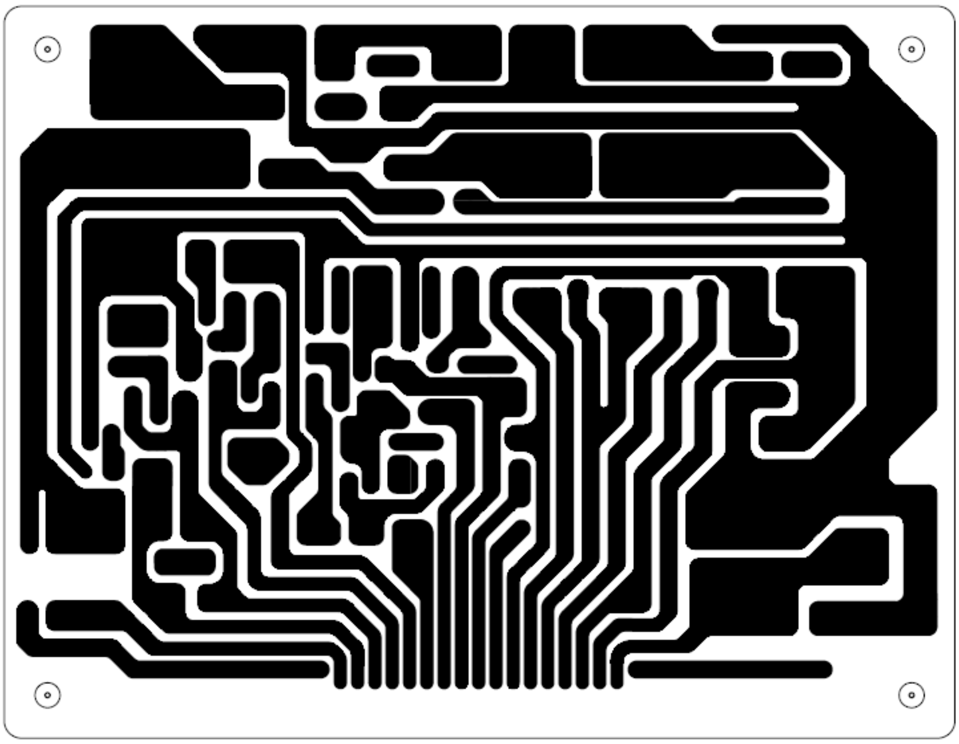


PCB Layout example

STK433-040N-E/060N-E/130N-E/330N-E

Top view





STK433-040N-E/060N-E/130N-E/330N-E PCB PARTS LIST

PCB Name : STK433 - 000Sr GEVB - A

Location No. (*2) 2ch Amp doesn't mount parts of ( ) .		RATING	Component		
Hybrid IC#1 Pin Position		-	STK433-		
			040N-E	060N-E	130N-E/ 330N-E
R01		100Ω, 1W	○		
R02, R03, (R04)		1kΩ, 1/6W	○		
R05, R06, (R07), R08, R09, (R10)		56KΩ, 1/6W	○		
R11, R12, (R13)		1.8KΩ, 1/6W	○		
R14, R15, (R16)		4.7Ω, 1/4W	○		
R17, R18, (R19)		4.7Ω, 1W	○		
R20, R21, (R22)		0.22Ω, 2W	○	○	-
		0.22Ω, 5W	-	-	○
C01, C02, C03, C23		100μF, 100V	○		
C04, C05, (C06)		2.2μF, 50V	○		
C07, C08, (C09)		470pF, 50V	○		
C10, C11, (C12)		3pF, 50V	○		
C13, C14, (C15)		10μF, 16V	○		
C16, C17, (C18)		0.1μF, 50V	○		
C19, C20, (C21)		***pF, 50V	100pF	56pF	N.C.
R34, R35, (R36)		Jumper	Short		
L01, L02, (L03)		3μH	○		
Stand-By Control Circuit	Tr1	VCE ≥ 75V, IC ≥ 1mA	○		
	D1	Di	○		
	R30 (*2)	2.7kΩ, 1/6W	○ (*2)		
	R31	33kΩ, 1/6W	○		
	R32	1kΩ, 1/6W	○		
	R33	2kΩ, 1/6W	○		
	C32	33μF, 10V	○		
J1, J2, J3, J4, J5, J6, J8, J9		Jumper	○		
J7, JS2, JS3, JS4, JS5, JS7 JS8, JS9		-	-		
JS6, JS10		Jumper	○		
JS1 (R23)		100Ω, 1W	○		

(*1) STK433-040N-E/060N-E/130N-E (2ch Amp) doesn't mount parts of ( )

(*2) Recommended standby circuit is used.

Recommended external components

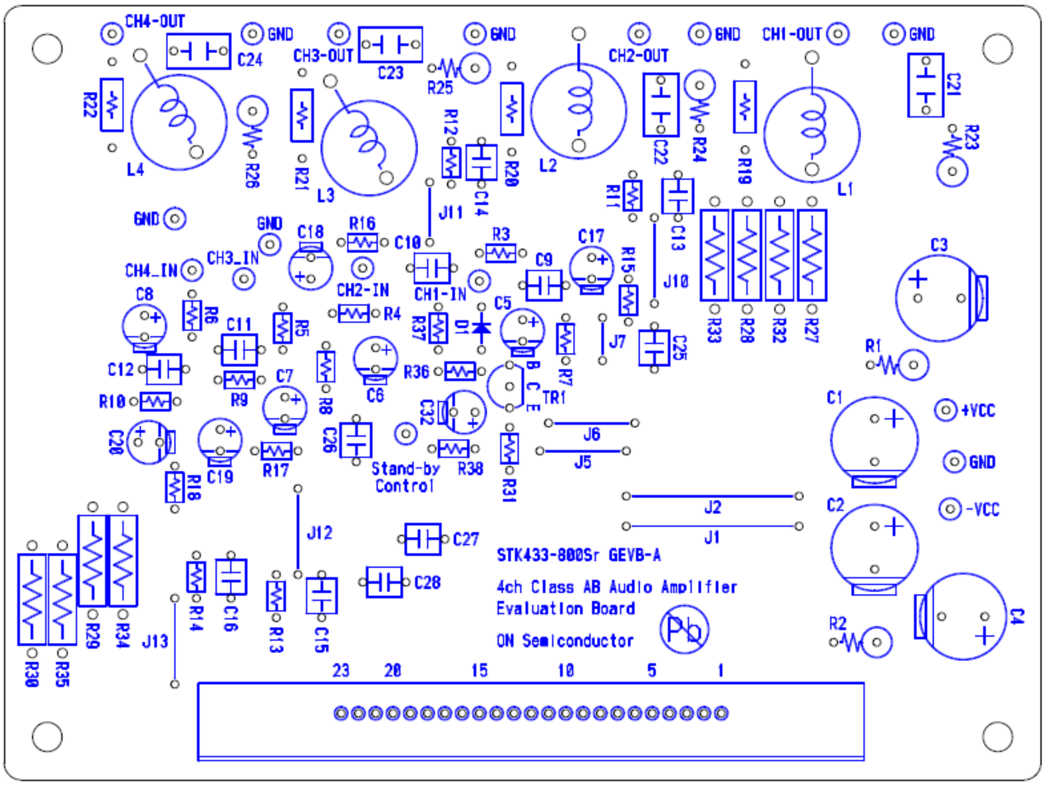
STK433-040N-E/060N-E/130N-E/330N-E

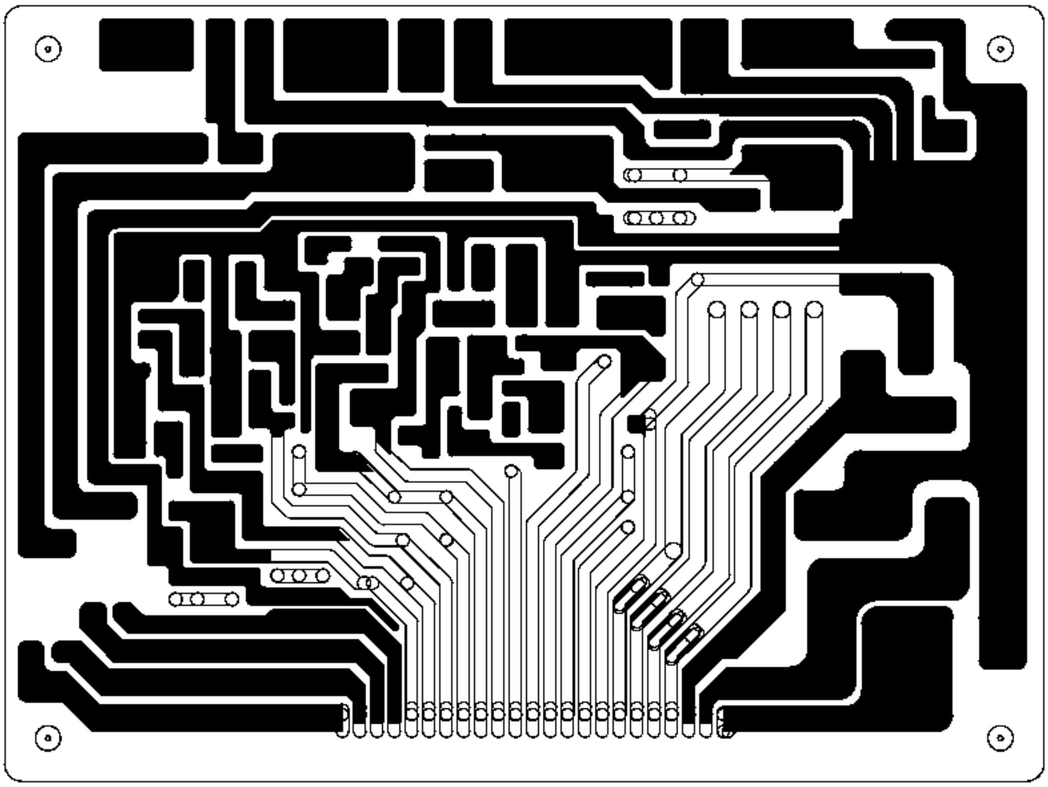
Parts Location	Recommended value	Circuit purpose	Above Recommended value	Below Recommended value
R01, R23	100Ω/1W	Resistance for Ripple filter. (Fuse resistance is recommended. Ripple filter is constituted with C03, C23.)	Short-through current may decrease at high frequency.	Short-through current may increase at high frequency.
R02, R03, R04	1kΩ	Resistance for input filters.	-	-
R05, R06, R07	56kΩ	Input impedance is determined.	Output neutral voltage(VN) shift. (It is referred that R05=R08, R06=R09)	
R08, R09, R10	56kΩ	Voltage Gain (VG) is determined with R11, R12, R13	-	-
R11, R12, R13	1.8kΩ	Voltage Gain (VG) is determined with R8, R9, R10 (As for VG, it is desirable to set up by R11, R12, R13)	It may oscillate. (Vg < 30dB)	With especially no problem
R14, R15, R16	4.7Ω	Resistance for oscillation prevention.	-	-
R17, R18, R19	4.7Ω/1W	Resistance for oscillation prevention.	-	-
R20, R21, R22	0.22Ω/2W (040N-E,060N-E) 0.22Ω/5W (130N-E,330N-E)	This resistance is used as detection resistance of the protection circuit application.	Decrease of Maximum output Power	It may cause thermal runaway
R30	Note *5	Select Restriction resistance, for the impression voltage of '#17 (Stand-By) pin' must not exceed the maximum rating.		
C01, C02	100μF/50V	Capacitor for oscillation prevention. • Locate near the HIC as much as possible. • Power supply impedance is lowered and stable operation of the IC is carried out. (Electrolytic capacitor is recommended.)	-	-
C03, C23	100μF/50V	Decoupling capacitor • The Ripple ingredient mixed in an input side Is removed from a power supply line. (Ripple filter is constituted with R01, R23.)	The change in the Ripple ingredient mixed in an input side from a power supply line	
C04, C05, C06	2.2μF/50V	Input coupling capacitor.(for DC current prevention.)	-	
C07, C08, C09	470pF	Input filter capacitor • A high frequency noise is reduced with the filter constituted by R02, R03, R04	-	
C10, C11, C12	3pF	Capacitor for oscillation prevention.	It may oscillate.	
C13, C14, C15	10μF/10V	Negative feedback capacitor. The cutoff frequency of a low cycle changes. (fL = 1/(2π · C13 · R11))	The voltage gain (VG) of low frequency is extended. However, the pop noise at the time of a power supply injection also becomes large.	The voltage gain (VG) of low frequency decreases.
C16, C17, C18	0.1μF	Capacitor for oscillation prevention.	It may oscillate.	
C19, C20, C21	100pF (040N-E) 56pF (060N-E) N.C. (130N-E, 330N-E)	Capacitor for oscillation prevention.	It may oscillate.	
L01, L02, L03	3μH	Coil for oscillation prevention.	With especially no problem	It may oscillate.

PCB Layout example

STK433-840N-E/890N-E

Top view





STK433-800NSr PCB PARTS LIST

PCB Name : STK433-800Sr GEVB - A

Location No.		RATING	Component
			STK433-840N-E/890N-E
Hybrid IC#1 Pin Position		-	1
R01, R02		100Ω, 1W	○
R03, R04, R05, R06		1kΩ, 1/6W	○
R07, R08, R09, R10, R11, R12, R13, R14		56KΩ, 1/6W	○
R15, R16, R17, R18		1.8KΩ, 1/6W	○
R19, R20, R21, R22		4.7Ω, 1/4W	○
R23, R24, R25, R26		4.7Ω, 1W	○
R27, R28, R29, R30		0.22Ω, 5W	○
R32, R33, R34, R35		Jumper	Short
C01, C02, C03, C04		100μF, 100V	○
C05, C06, C07, C08		2.2μF, 50V	○
C09, C10, C11, C12		470pF, 50V	○
C13, C14, C15, C16		5pF, 50V	○
C17, C18, C19, C20		10μF, 16V	○
C21, C22, C23, C24		0.1μF, 50V	○
C25, C26, C27, C28		100pF, 50V	○
L01, L02, L03, L04		3μH	○
Stand-By Control Circuit	Tr1	VCE ≥ 50V, IC ≥ 10mA	○
	D1	Di	○
	R31	1.3kΩ, 1/6W	○
	R36	33kΩ, 1/6W	○
	R37	1kΩ, 1/6W	○
	R38	2kΩ, 1/6W	○
	C32	33μF, 10V	○
J1,J2,J5,J6,J7,J10,J11,J12,J13		Jumper	○
		-	
		-	
		-	

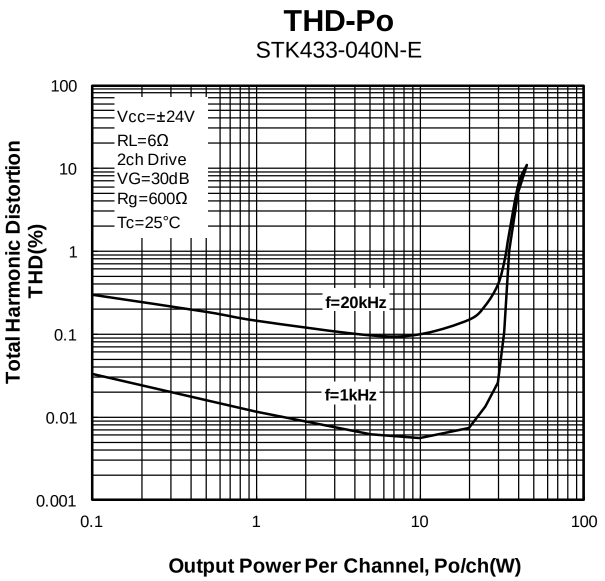
Recommended external components

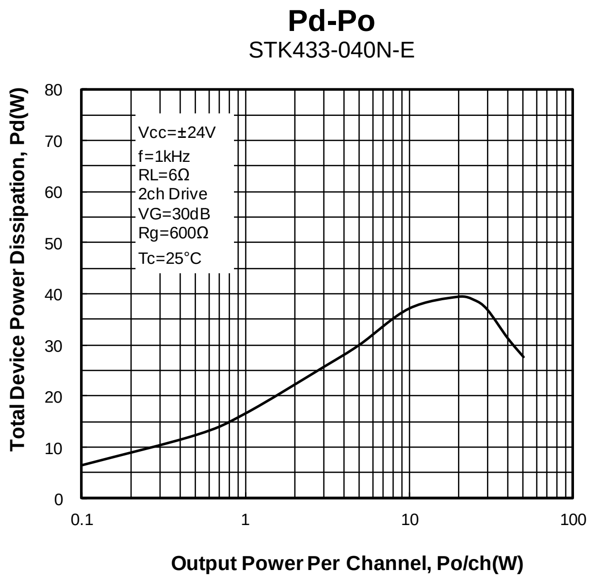
STK433-840N-E/890N-E

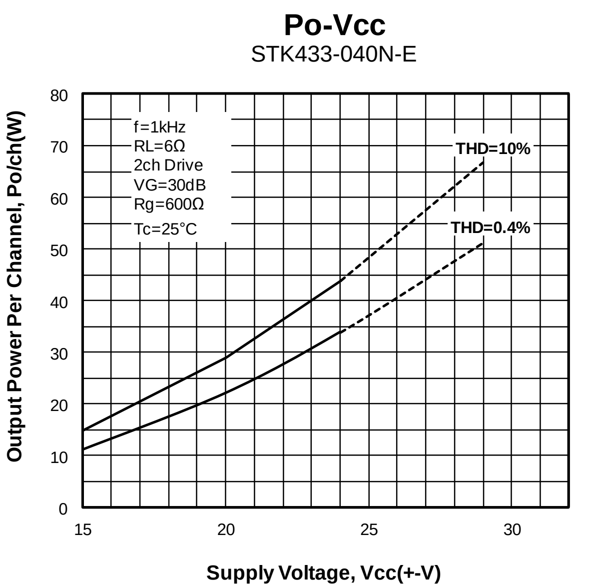
Parts Location	Recommended value	Circuit purpose	Above Recommended value	Below Recommended value
R01, R02	100Ω/1W	Resistance for Ripple filters. (Fuse resistance is recommended. Ripple filter is constituted with C03, C04.)	Short-through current may decrease at high frequency.	Short-through current may increase at high frequency.
R03,R04,R05, R06	1kΩ	Resistance for input filters.	-	-
R07,R08,R09, R10	56kΩ	Input impedance is determined.	Output neutral voltage (VN) shift. (It is referred that R07=R11, R08=R12, R09=R13, R10=R14)	
R11,R12,R13, R14	56kΩ	Voltage Gain (VG) is determined with R15, R16, R17, R18	-	-
R15,R16,R17, R18	1.8kΩ	Voltage Gain (VG) is determined with R11, R12, R13, and R14. (As for VG, it is desirable to set up by R15, R16, R17, and R18.)	It may oscillate. (Vg < 30dB)	With especially no problem
R19,R20,R21, R22	4.7Ω	Resistance for oscillation prevention.	-	-
R23,R24,R25, R26	4.7Ω/1W	Resistance for oscillation prevention.	-	-
R27,R28,R29, R30	0.22Ω ±10%, 5W	Output emitter resistor (Metal-plate Resistor is recommended.)	Decrease of Maximum output Power	It may cause thermal runaway
R31	Note *4	Select Restriction resistance, for the impression voltage of '#13 (Stand-By) pin' must not exceed the maximum rating.		
C01, C02	100μF/100V	Capacitor for oscillation prevention. • Locate near the HIC as much as possible. • Power supply impedance is lowered and stable operation of the IC is carried out. (Electrolytic capacitor is recommended.)	-	-
C03,C04	100μF/100V	Decoupling capacitor • The Ripple ingredient mixed in an input side Is removed from a power supply line. (Ripple filter is constituted with R01, R02.)	The change in the Ripple ingredient mixed in an input side from a power supply line	
C05,C06,C07, C08	2.2μF/50V	Input coupling capacitor. (For DC current prevention.)	-	
C09,C10,C11, C12	470pF	Input filter capacitor • A high frequency noise is reduced with the filter constituted by R03, R04, R05, R06.	-	
C13,C14,C15, C16	5pF	Capacitor for oscillation prevention.	It may oscillate.	
C17,C18,C19, C20	10μF/10V	Negative feedback capacitor. The cutoff frequency of a low cycle changes. (fL = 1/(2π · C17 · R15))	The voltage gain (VG) of low frequency is extended. However, the pop noise at the time of a power supply injection also becomes large.	The voltage gain (VG) of low frequency decreases.
C21,C22,C23, C24	0.1μF	Capacitor for oscillation prevention.	It may oscillate.	
C25,C26,C27, C28	100pF	Capacitor for oscillation prevention.	It may oscillate.	
L01,L02,L03, L04	3μH	Coil for oscillation prevention.	With especially no problem	It may oscillate.

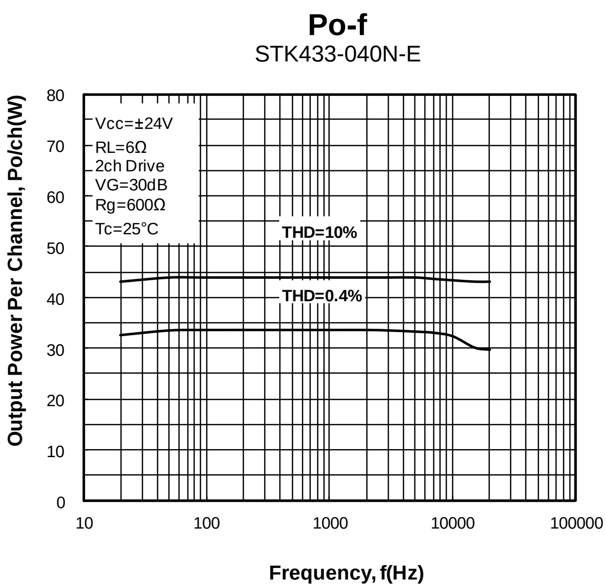
Characteristic of Evaluation Board

STK433-040N-E



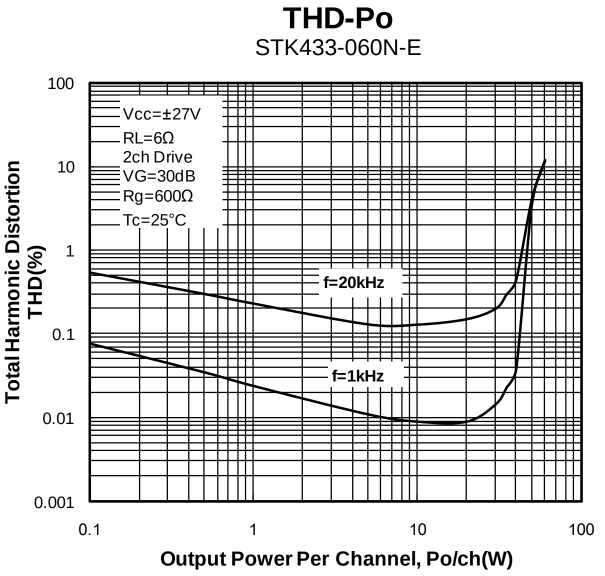


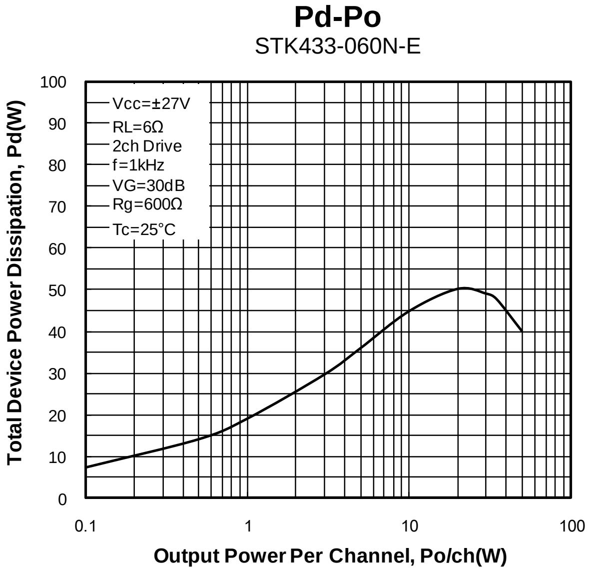


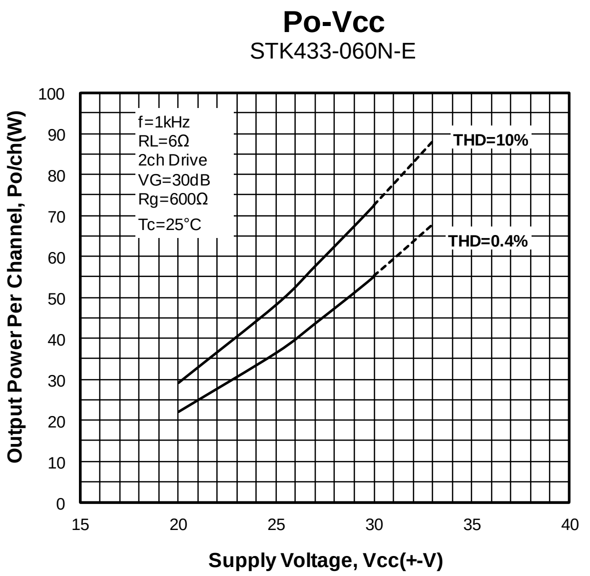


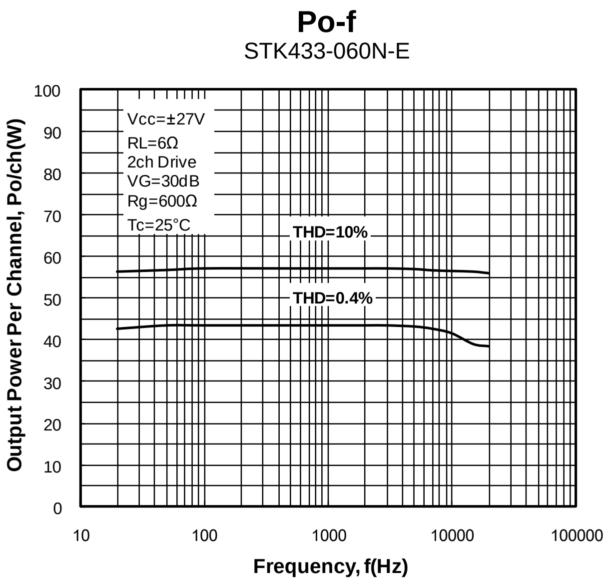
Characteristic of Evaluation Board

STK433-060N-E



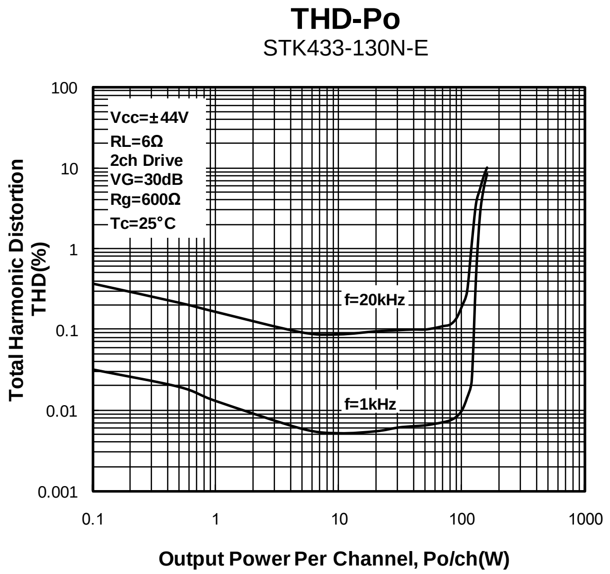


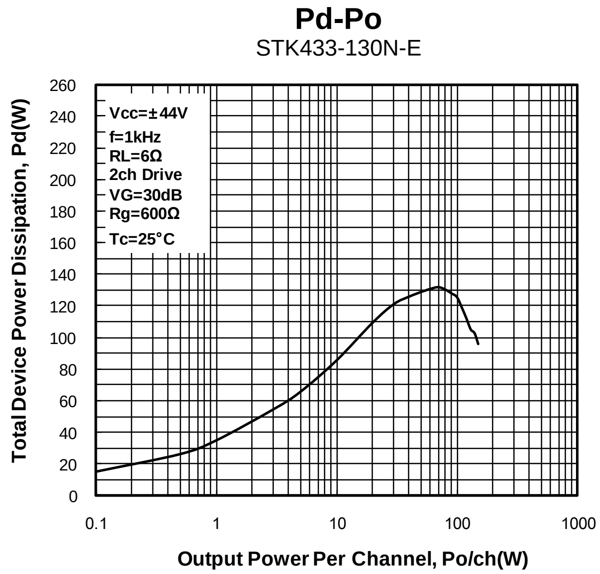


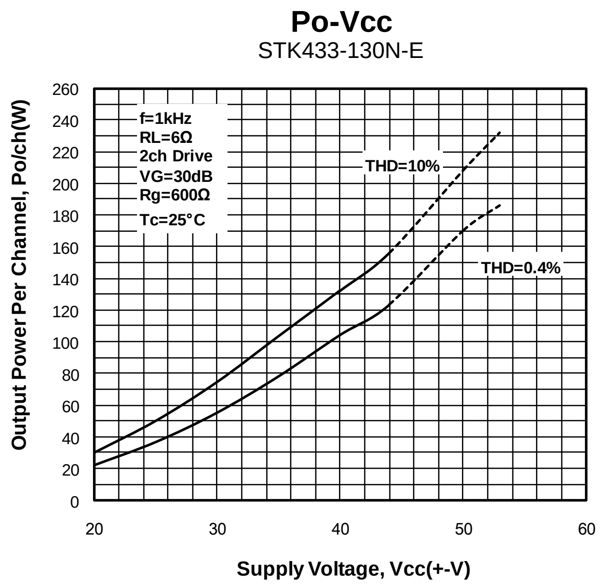


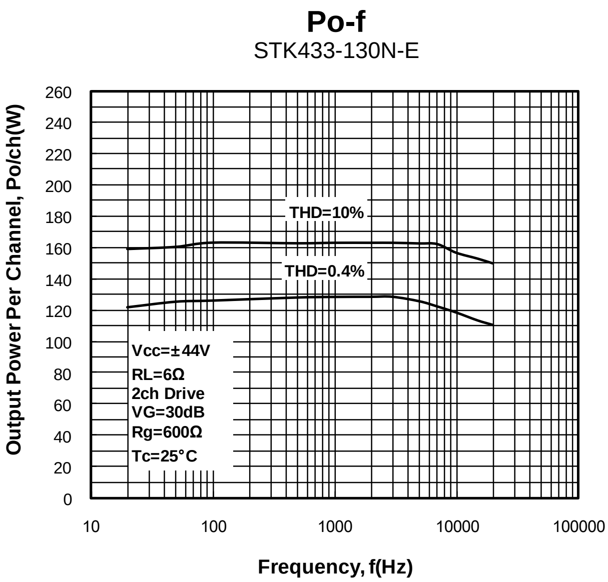
Characteristic of Evaluation Board

STK433-130N-E



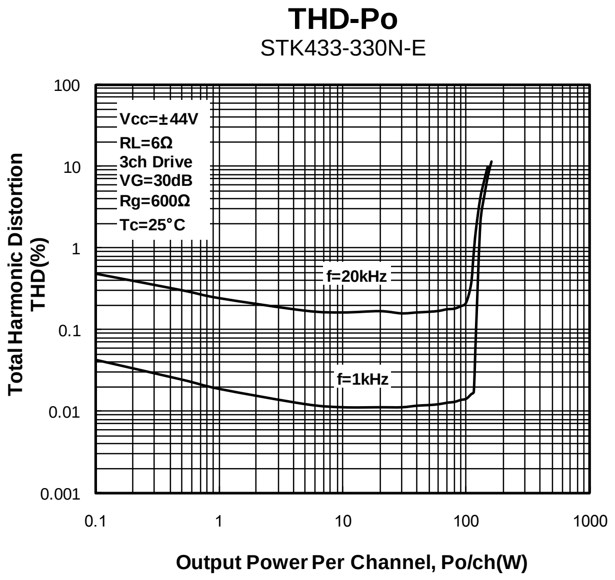


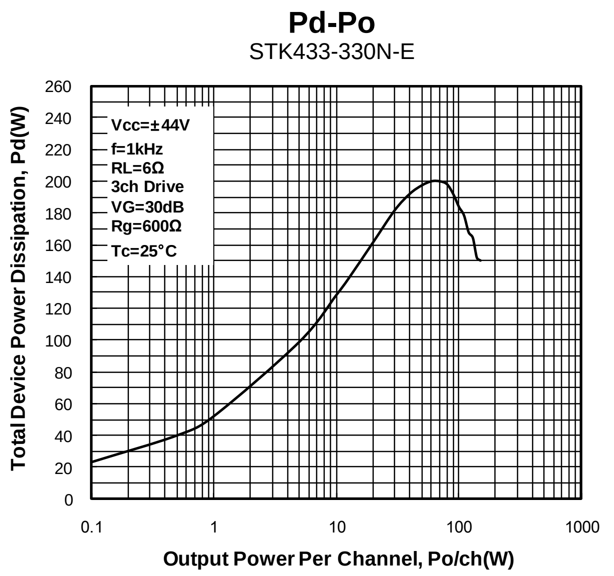


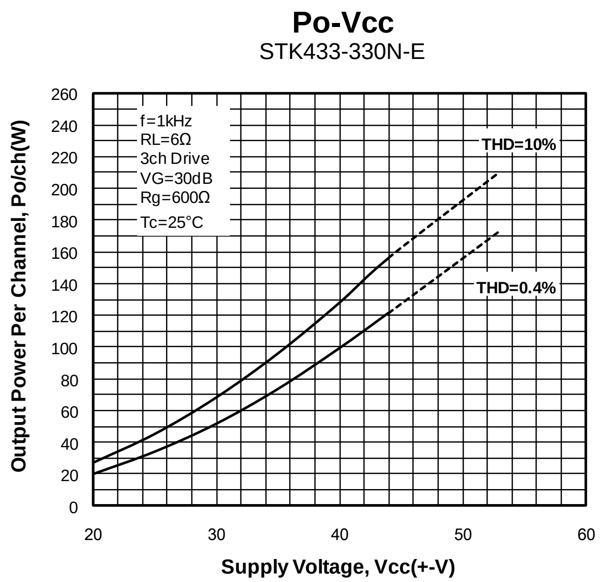


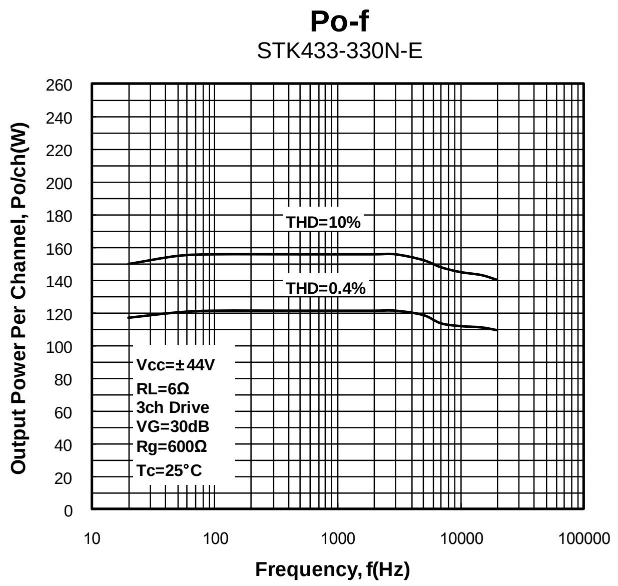
Characteristic of Evaluation Board

STK433-330N-E





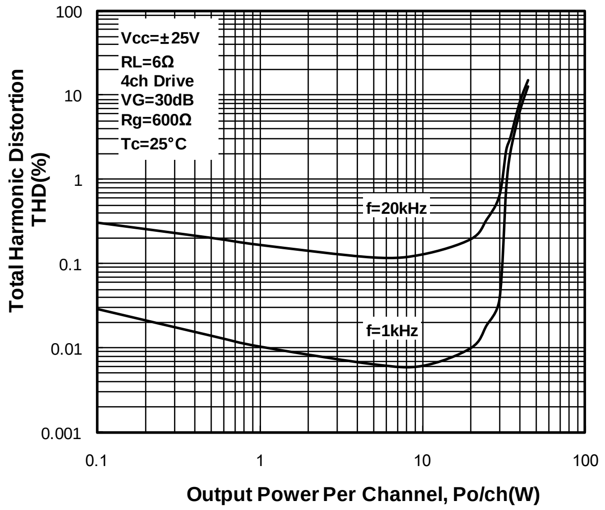




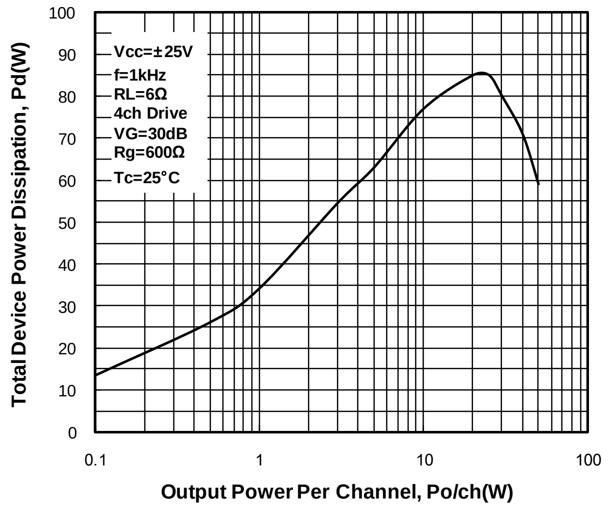
Characteristic of Evaluation Board

STK433-840N-E

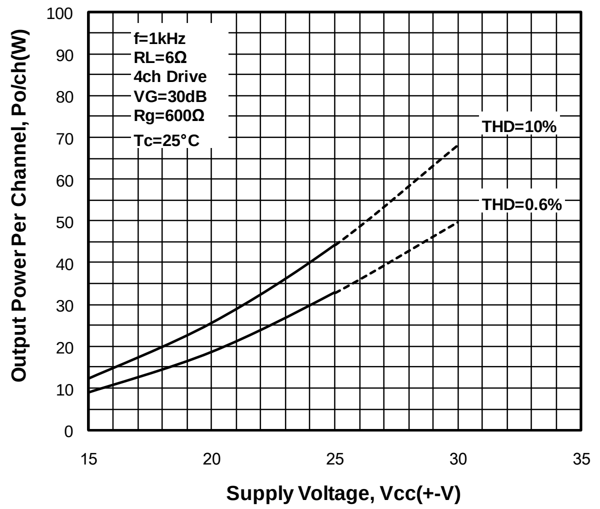
THD-Po
STK433-840N-E



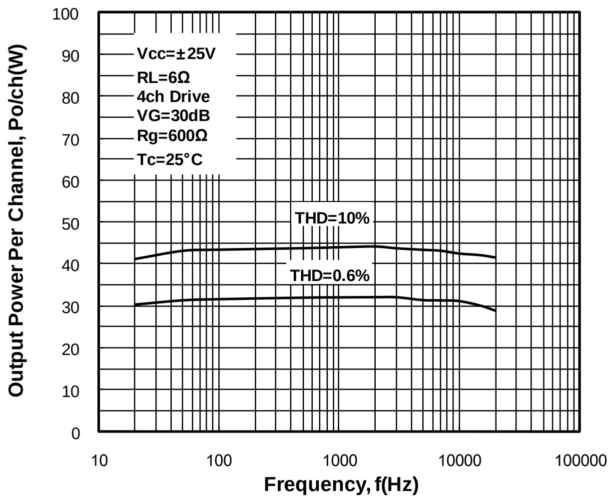
Pd-Po
STK433-840N-E



Po-Vcc
STK433-840N-E

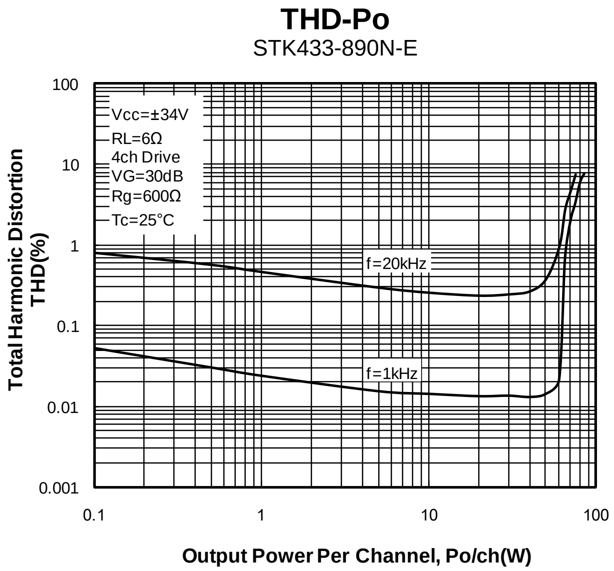


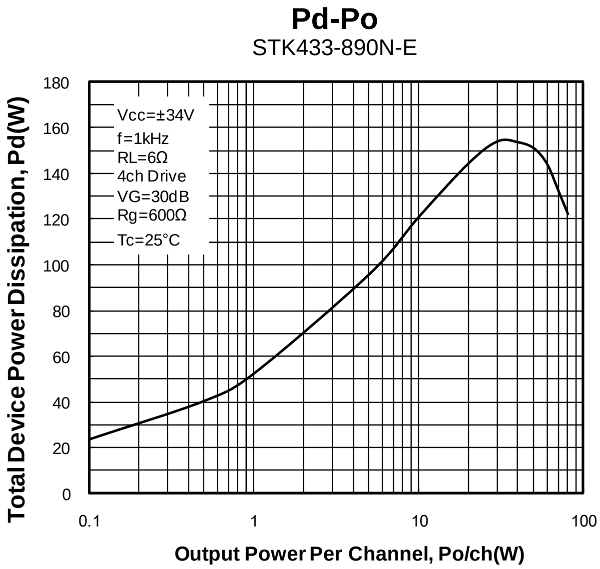
Po-f
STK433-840N-E

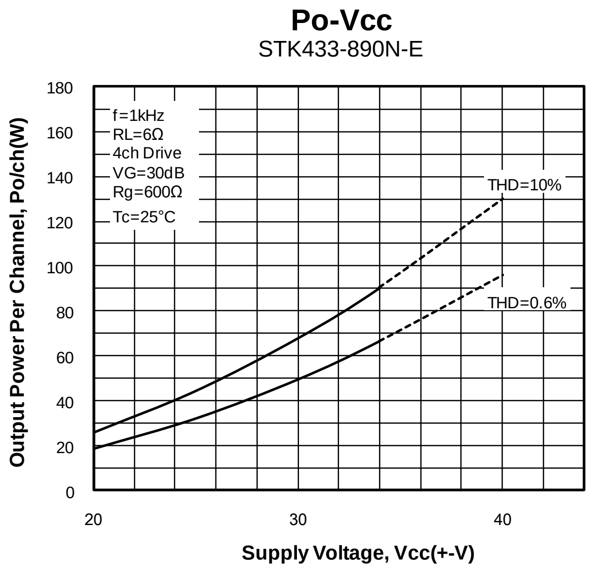


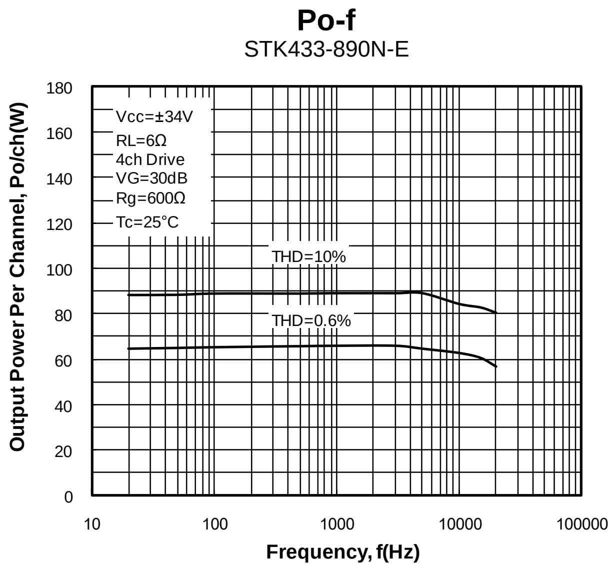
Characteristic of Evaluation Board

STK433-890N-E









A Thermal Design Tip For STK433-040N-E Amplifier

[Thermal Design Conditions]

The thermal resistance (θc-a) of the heat-sink which manages the heat dissipation inside the Hybrid IC will be determined as follow:

(Condition 1) The case temperature (Tc) of the Hybrid IC should not exceed 125°C

$Pd \times \theta c\text{-}a + Ta < 125°C$ ···························(1)

Where Ta : the ambient temperature for the system

(Condition 2) The junction temperature of each power transistor should not exceed 150°C

$Pd \times \theta c\text{-}a + Pd/N \times \theta j\text{-}c + Ta < 150°C$ ···························(2)

Where N : the number of transistors (two for 1 channel , ten for channel)

θj-c : the thermal resistance of each transistor (see specification)

Note that the power consumption of each power transistor is assumed to be equal to the total power dissipation (Pd) divided by the number of transistors (N).

From the formula (1) and (2), we will obtain:

$\theta c\text{-}a < (125 - Ta)/Pd$ ···························(1)'

$\theta c\text{-}a < (150 - Ta)/Pd - \theta j\text{-}c/N$ ···························(2)'

The value which satisfies above formula (1)' and (2)' will be the thermal resistance for a desired heat-sink.

Note that all of the component except power transistors employed in the Hybrid IC comply with above conditions.

[Example of Thermal Design]

Generally, the power consumption of actual music signals are being estimated by the continuous signal of 1/8 P_O max. (Note that the value of 1/8 P_O max may be varied from the country to country.)

(Sample of STK433-040N-E ; 25W×2ch)

If V_{CC} is ±24V, and R_L is 6Ω, then the total power dissipation (Pd) of inside Hybrid IC is as follow;

Pd = 26W (at 3.13W output power,1/8 of P_O max)

There are four (4) transistors in Audio Section of this Hybrid IC, and thermal resistance (θj-c) of each transistor is 4.2°C/W. If the ambient temperature (Ta) is guaranteed for 50°C, then the thermal resistance (θc-a) of a desired heat-sink should be;

From (1)' $\theta c\text{-}a < (125 - 50)/26$

< 2.88

From (2)' $\theta c\text{-}a < (150 - 50)/26 - 4.2/4$

< 2.79

Therefore, in order to satisfy both (1)' and (2)', the thermal resistance of a desired Heat-sink will be 2.79°C/W.

[Note]

Above are reference only. The samples are operated with a constant power supply. Please verify the conditions when your system is actually implemented.

A Thermal Design Tip For STK433-060N-E Amplifier

[Thermal Design Conditions]

The thermal resistance (θc-a) of the heat-sink which manages the heat dissipation inside the Hybrid IC will be determined as follow:

(Condition 1) The case temperature (Tc) of the Hybrid IC should not exceed 125°C

$Pd \times \theta c\text{-}a + Ta < 125°C$ ······ (1)

Where Ta : the ambient temperature for the system

(Condition 2) The junction temperature of each power transistor should not exceed 150°C

$Pd \times \theta c\text{-}a + Pd/N \times \theta j\text{-}c + Ta < 150°C$ ······ (2)

Where N : the number of transistors (two for 1 channel , ten for channel)

θj-c : the thermal resistance of each transistor (see specification)

Note that the power consumption of each power transistor is assumed to be equal to the total power dissipation (Pd) divided by the number of transistors (N).

From the formula (1) and (2), we will obtain:

$\theta c\text{-}a < (125 - Ta)/Pd$ ······ (1)’

$\theta c\text{-}a < (150 - Ta)/Pd - \theta j\text{-}c/N$ ······ (2)’

The value which satisfies above formula (1)’ and (2)’ will be the thermal resistance for a desired heat-sink.

Note that all of the component except power transistors employed in the Hybrid IC comply with above conditions.

[Example of Thermal Design]

Generally, the power consumption of actual music signals are being estimated by the continuous signal of 1/8 P_O max. (Note that the value of 1/8 P_O max may be varied from the country to country.)

(Sample of STK433-060N-E ; 35W×2ch)

If V_{CC} is ±27V, and R_L is 6Ω, then the total power dissipation (Pd) of inside Hybrid IC is as follow;

Pd = 33W (at 4.375W output power,1/8 of P_O max)

There are four (4) transistors in Audio Section of this Hybrid IC, and thermal resistance (θj-c) of each transistor is 3.5°C/W. If the ambient temperature (Ta) is guaranteed for 50°C, then the thermal resistance (θc-a) of a desired heat-sink should be;

From (1)’ $\theta c\text{-}a < (125 - 50)/33$

< 2.27

From (2)’ $\theta c\text{-}a < (150 - 50)/33 - 3.5/4$

< 2.16

Therefore, in order to satisfy both (1)’ and (2)’, the thermal resistance of a desired Heat-sink will be 2.16°C/W.

[Note]

Above are reference only. The samples are operated with a constant power supply. Please verify the conditions when your system is actually implemented.

A Thermal Design Tip For STK433-130N-E Amplifier

[Thermal Design Conditions]

The thermal resistance (θc-a) of the heat-sink which manages the heat dissipation inside the Hybrid IC will be determined as follow:

(Condition 1) The case temperature (Tc) of the Hybrid IC should not exceed 125°C

$$Pd \times \theta c\text{-}a + Ta < 125°C \cdots\cdots (1)$$

Where Ta : the ambient temperature for the system

(Condition 2) The junction temperature of each power transistor should not exceed 150°C

$$Pd \times \theta c\text{-}a + Pd/N \times \theta j\text{-}c + Ta < 150°C \cdots\cdots (2)$$

Where N : the number of transistors (two for 1 channel , ten for channel)

θj-c : the thermal resistance of each transistor (see specification)

Note that the power consumption of each power transistor is assumed to be equal to the total power dissipation (Pd) divided by the number of transistors (N).

From the formula (1) and (2), we will obtain:

$$\theta c\text{-}a < (125 - Ta)/Pd \cdots\cdots (1)'$$

$$\theta c\text{-}a < (150 - Ta)/Pd - \theta j\text{-}c/N \cdots\cdots (2)'$$

The value which satisfies above formula (1)' and (2)' will be the thermal resistance for a desired heat-sink.

Note that all of the component except power transistors employed in the Hybrid IC comply with above conditions.

[Example of Thermal Design]

Generally, the power consumption of actual music signals are being estimated by the continuous signal of 1/8 P_O max. (Note that the value of 1/8 P_O max may be varied from the country to country.)

(Sample of STK433-130N-E ; 100W×2ch)

If V_{CC} is ±44V, and R_L is 6Ω, then the total power dissipation (Pd) of inside Hybrid IC is as follow;

Pd = 91W (at 12.5W output power,1/8 of P_O max)

There are four (4) transistors in Audio Section of this Hybrid IC, and thermal resistance (θj-c) of each transistor is 1.6°C/W. If the ambient temperature (Ta) is guaranteed for 50°C, then the thermal resistance (θc-a) of a desired heat-sink should be;

From (1)' $\theta c\text{-}a < (125 - 50)/91$

< 0.82

From (2)' $\theta c\text{-}a < (150 - 50)/91 - 1.6/4$

< 0.70

Therefore, in order to satisfy both (1)' and (2)', the thermal resistance of a desired Heat-sink will be 0.70°C/W.

[Note]

Above are reference only. The samples are operated with a constant power supply. Please verify the conditions when your system is actually implemented.

A Thermal Design Tip For STK433-330N-E Amplifier

[Thermal Design Conditions]

The thermal resistance (θc-a) of the heat-sink which manages the heat dissipation inside the Hybrid IC will be determined as follow:

(Condition 1) The case temperature (Tc) of the Hybrid IC should not exceed 125°C

Pd × θc-a + Ta < 125°C ··(1)

Where Ta : the ambient temperature for the system

(Condition 2) The junction temperature of each power transistor should not exceed 150°C

Pd × θc-a + Pd/N × θj-c + Ta < 150°C ··································(2)

Where N : the number of transistors (two for 1 channel , ten for channel)

θj-c : the thermal resistance of each transistor (see specification)

Note that the power consumption of each power transistor is assumed to be equal to the total power dissipation (Pd) divided by the number of transistors (N).

From the formula (1) and (2), we will obtain:

θc-a < (125 – Ta)/Pd ··(1)'

θc-a < (150 – Ta)/Pd – θj-c/N ·····································(2)'

The value which satisfies above formula (1)' and (2)' will be the thermal resistance for a desired heat-sink.

Note that all of the component except power transistors employed in the Hybrid IC comply with above conditions.

[Example of Thermal Design]

Generally, the power consumption of actual music signals are being estimated by the continuous signal of 1/8 P_O max. (Note that the value of 1/8 P_O max may be varied from the country to country.)

(Sample of STK433-330N-E ; 100W×3ch)

If V_{CC} is ±44V, and R_L is 6Ω, then the total power dissipation (Pd) of inside Hybrid IC is as follow;

Pd = 139W (at 12.5W output power,1/8 of P_O max)

There are six (6) transistors in Audio Section of this Hybrid IC, and thermal resistance (θj-c) of each transistor is 1.6°C/W. If the ambient temperature (Ta) is guaranteed for 50°C, then the thermal resistance (θc-a) of a desired heat-sink should be;

From (1)' θc-a < (125 – 50)/139
< 0.54

From (2)' θc-a < (150 – 50)/139 – 1.6/6
< 0.45

Therefore, in order to satisfy both (1)' and (2)', the thermal resistance of a desired Heat-sink will be 0.45°C/W.

[Note]

Above are reference only. The samples are operated with a constant power supply. Please verify the conditions when your system is actually implemented.

A Thermal Design Tip For STK433-840N-E Amplifier

[Thermal Design Conditions]

The thermal resistance (θc-a) of the heat-sink which manages the heat dissipation inside the Hybrid IC will be determined as follow:

(Condition 1) The case temperature (Tc) of the Hybrid IC should not exceed 125°C

Pd × θc-a + Ta < 125°C ·····························(1)

Where Ta : the ambient temperature for the system

(Condition 2) The junction temperature of each power transistor should not exceed 150°C

Pd × θc-a + Pd/N × θj-c + Ta < 150°C ·····························(2)

Where N : the number of transistors (two for 1 channel , ten for channel)

θj-c : the thermal resistance of each transistor (see specification)

Note that the power consumption of each power transistor is assumed to be equal to the total power dissipation (Pd) divided by the number of transistors (N).

From the formula (1) and (2), we will obtain:

θc-a < (125 – Ta)/Pd·····························(1)’

θc-a < (150 – Ta)/Pd – θj-c/N ·····························(2)’

The value which satisfies above formula (1)’ and (2)’ will be the thermal resistance for a desired heat-sink.

Note that all of the component except power transistors employed in the Hybrid IC comply with above conditions.

[Example of Thermal Design]

Generally, the power consumption of actual music signals are being estimated by the continuous signal of 1/8 P_O max. (Note that the value of 1/8 P_O max may be varied from the country to country.)

(Sample of STK433-840N-E ; 25W×4ch)

If V_{CC} is ±25V, and R_L is 6Ω, then the total power dissipation (Pd) of inside Hybrid IC is as follow;

Pd = 55.0W (at 3.125W output power,1/8 of P_O max)

There are eight (8) transistors in Audio Section of this Hybrid IC, and thermal resistance (θj-c) of each transistor is 4.2°C/W. If the ambient temperature (Ta) is guaranteed for 50°C, then the thermal resistance (θc-a) of a desired heat-sink should be;

From (1)’ θc-a < (125 – 50)/55.0
< 1.36

From (2)’ θc-a < (150 – 50)/55.0 – 4.2/8
< 1.29

Therefore, in order to satisfy both (1)’ and (2)’, the thermal resistance of a desired Heat-sink will be 1.29°C/W.

[Note]

Above are reference only. The samples are operated with a constant power supply. Please verify the conditions when your system is actually implemented.

A Thermal Design Tip For STK433-890N-E Amplifier

[Thermal Design Conditions]

The thermal resistance (θc-a) of the heat-sink which manages the heat dissipation inside the Hybrid IC will be determined as follow:

(Condition 1) The case temperature (Tc) of the Hybrid IC should not exceed 125°C

$Pd \times \theta c\text{-}a + Ta < 125°C$ ··························(1)

Where Ta : the ambient temperature for the system

(Condition 2) The junction temperature of each power transistor should not exceed 150°C

$Pd \times \theta c\text{-}a + Pd/N \times \theta j\text{-}c + Ta < 150°C$ ··························(2)

Where N : the number of transistors (two for 1 channel , ten for channel)

θj-c : the thermal resistance of each transistor (see specification)

Note that the power consumption of each power transistor is assumed to be equal to the total power dissipation (Pd) divided by the number of transistors (N).

From the formula (1) and (2), we will obtain:

$\theta c\text{-}a < (125 - Ta)/Pd$ ··························(1)'

$\theta c\text{-}a < (150 - Ta)/Pd - \theta j\text{-}c/N$ ··························(2)'

The value which satisfies above formula (1)' and (2)' will be the thermal resistance for a desired heat-sink.

Note that all of the component except power transistors employed in the Hybrid IC comply with above conditions.

[Example of Thermal Design]

Generally, the power consumption of actual music signals are being estimated by the continuous signal of 1/8 P_O max. (Note that the value of 1/8 P_O max may be varied from the country to country.)

(Sample of STK433-890N-E ; 50W×4ch)

If V_{CC} is ±34V, and R_L is 6Ω, then the total power dissipation (Pd) of inside Hybrid IC is as follow;

Pd = 99.0W (at 6.25W output power,1/8 of P_O max)

There are eight (8) transistors in Audio Section of this Hybrid IC, and thermal resistance (θj-c) of each transistor is 2.1°C/W. If the ambient temperature (Ta) is guaranteed for 50°C, then the thermal resistance (θc-a) of a desired heat-sink should be;

From (1)' $\theta c\text{-}a < (125 - 50)/99.0$

< 0.76

From (2)' $\theta c\text{-}a < (150 - 50)/99.0 - 2.1/8$

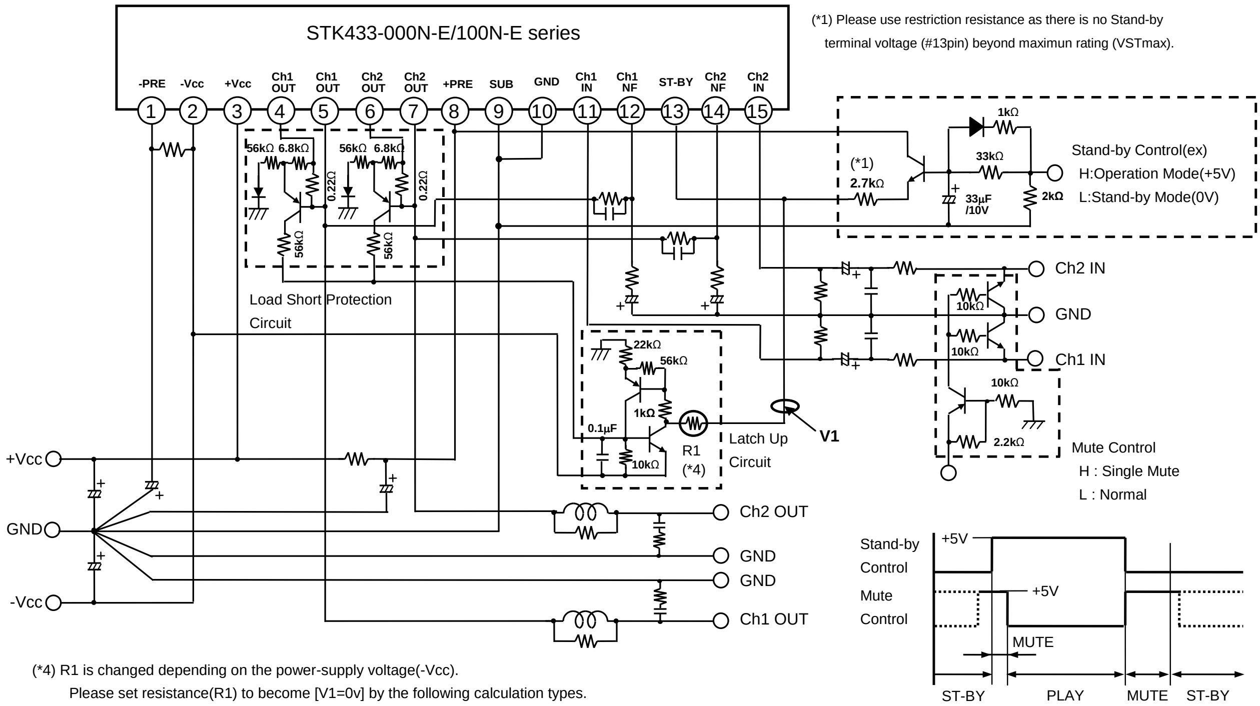
< 0.75

Therefore, in order to satisfy both (1)' and (2)', the thermal resistance of a desired Heat-sink will be 0.75°C/W.

[Note]

Above are reference only. The samples are operated with a constant power supply. Please verify the conditions when your system is actually implemented.

STK433-000N-E/100N-E series Stand-by Control & Mute Control & Load-Short Protection Application

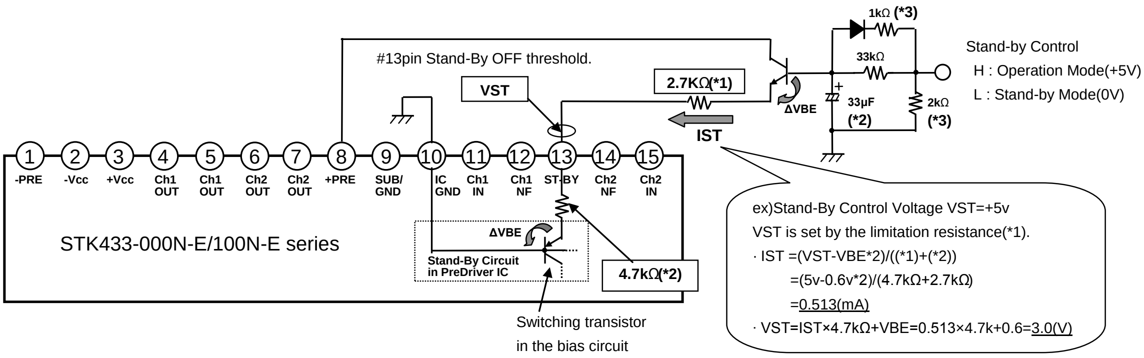


[STK433-000N-E/100N-E series Stand-By Control Example]

[Feature]

- The pop noise which occurs to the time of power supply on/off can be improved substantially by recommendation Stand-By Control Application.
- Stand-By Control can be done by additionally adjusting the limitation resistance to the voltage such as micom, the set design is easy.

(Reference circuit) STK433-000N-E/100N-E series test circuit To Stand-By Control added +5V.



[Operation explanation] #13pin Stand-By Control Voltage VST

(1) Operation Mode

The switching transistor in the bias circuit turns on and places the amplifier into the operating mode, when 13pin (VST) voltage added above 2.5V (typ 3.0V).

(2) Stand-By Mode

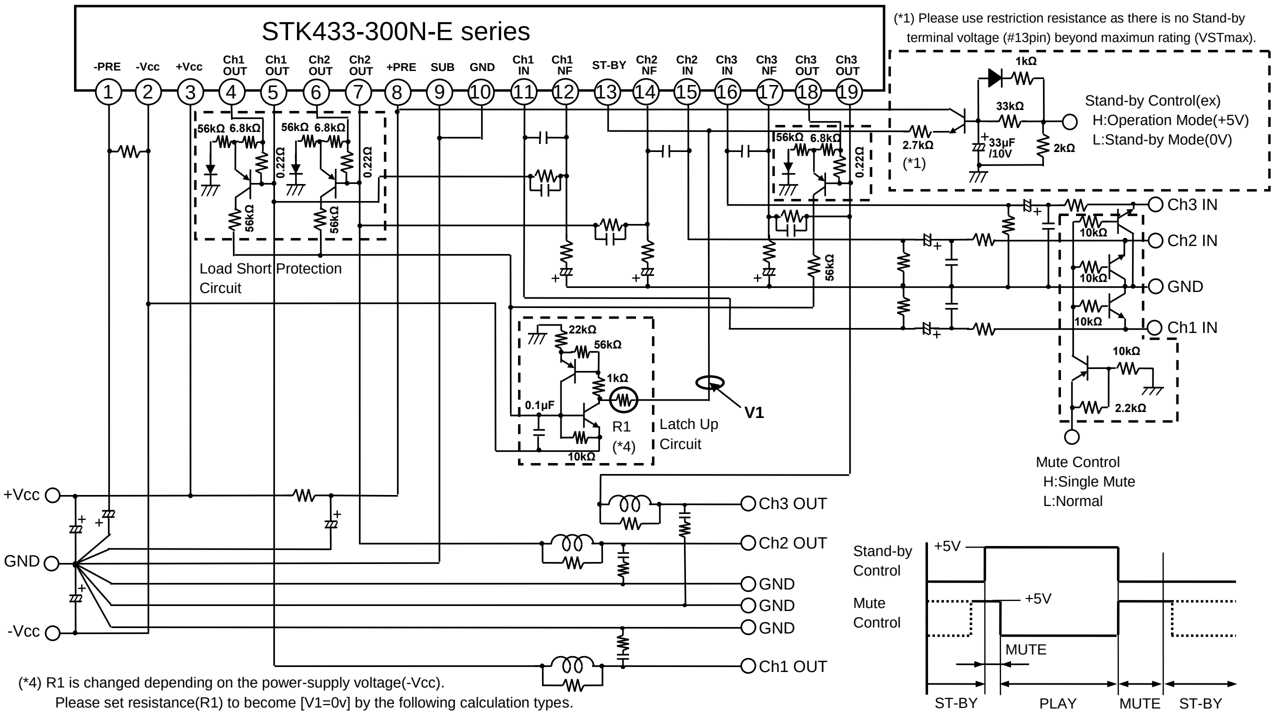
When 13pin (VST) voltage is stopped (= 0V), the switching transistor in the bias circuit turn off, placing the amplifier into the standby mode.

(*1) The current limiting resistor must be used to ensure that stand-by pin (13pin) voltage does not exceed its maximum rated value VST max.

(*2) The pop noise level when the power is turned on can be reduced by setting the time constant with a capacitor in operating mode.

(*3) Determines the time constant at which the capacitor (*2) is discharged in stand-by mode.

STK433-300 series Stand-by Control & Mute Control & Load-Short Protection Application

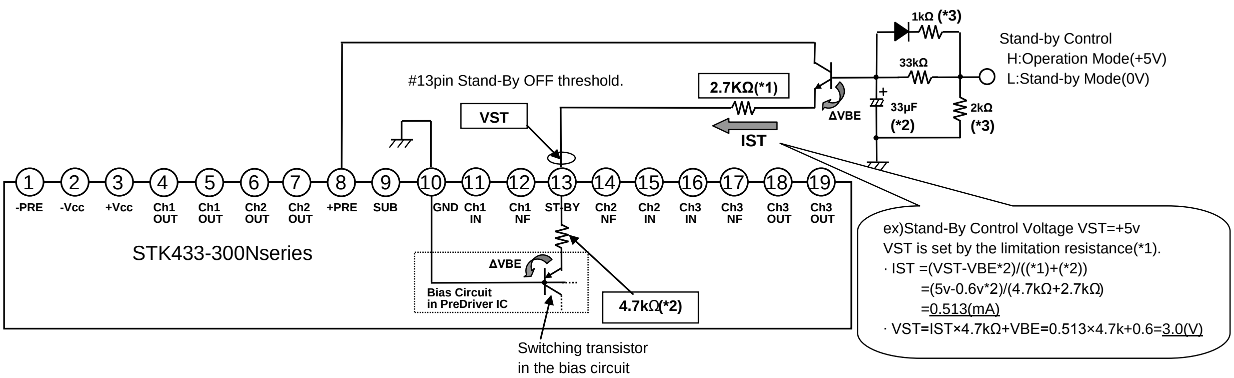


[STK433-300N-E series Stand-By Control Example]

[Feature]

- The pop noise which occurs to the time of power supply on/off can be improved substantially by recommendation Stand-By Control Application.
- Stand-By Control can be done by additionally adjusting the limitation resistance to the voltage such as micom, the set design is easy.

(Reference circuit) STK433-300N-E series test circuit To Stand-By Control added +5V.



[Operation explanation] #13pin Stand-By Control Voltage VST

(1) Operation Mode

The switching transistor in the bias circuit turns on and places the amplifier into the operating mode, when 13pin (VST) voltage added above 2.5V (typ 3.0V).

(2) Stand-By Mode

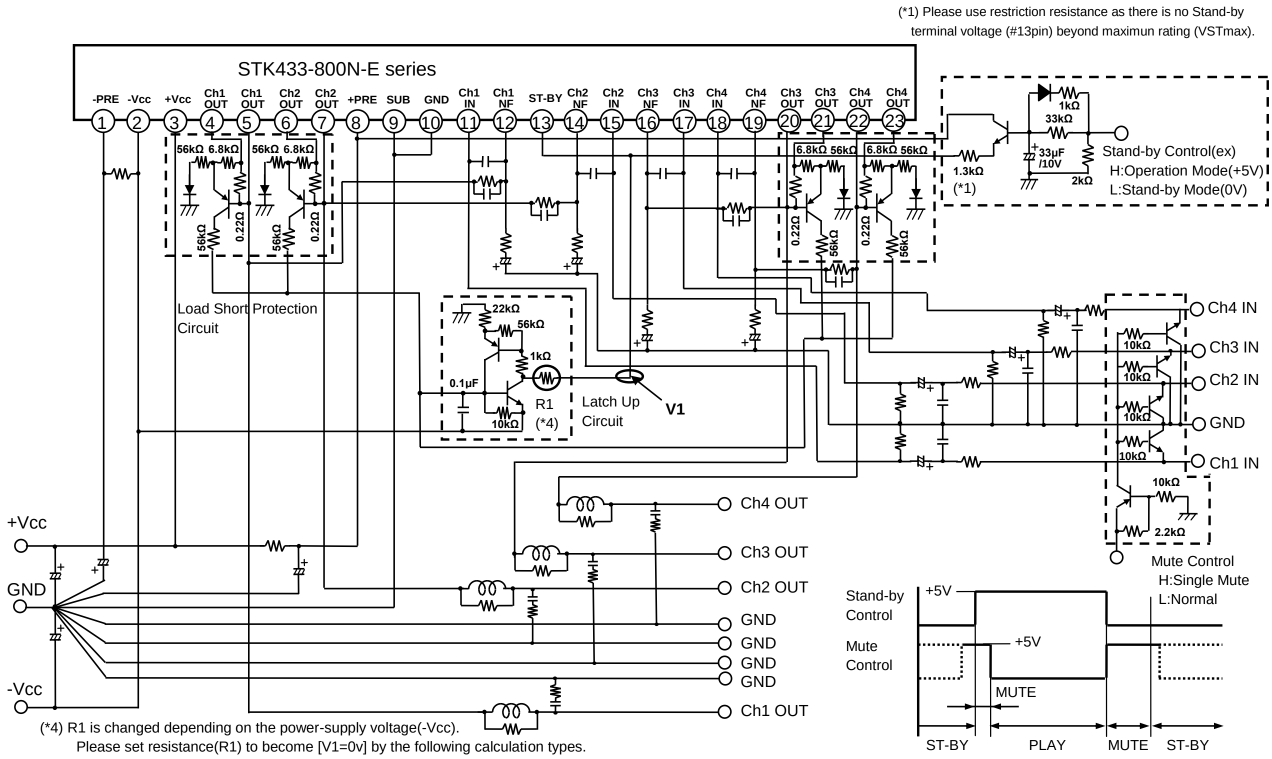
When 13pin (VST) voltage is stopped (= 0V), the switching transistor in the bias circuit turn off, placing the amplifier into the standby mode.

(*1) The current limiting resistor must be used to ensure that stand-by pin (13pin) voltage does not exceed its maximum rated value VST max.

(*2) The pop noise level when the power is turned on can be reduced by setting the time constant with a capacitor in operating mode.

(*3) Determines the time constant at which the capacitor (*2) is discharged in stand-by mode.

STK433-800 series Stand-by Control & Mute Control & Load-Short Protection Application

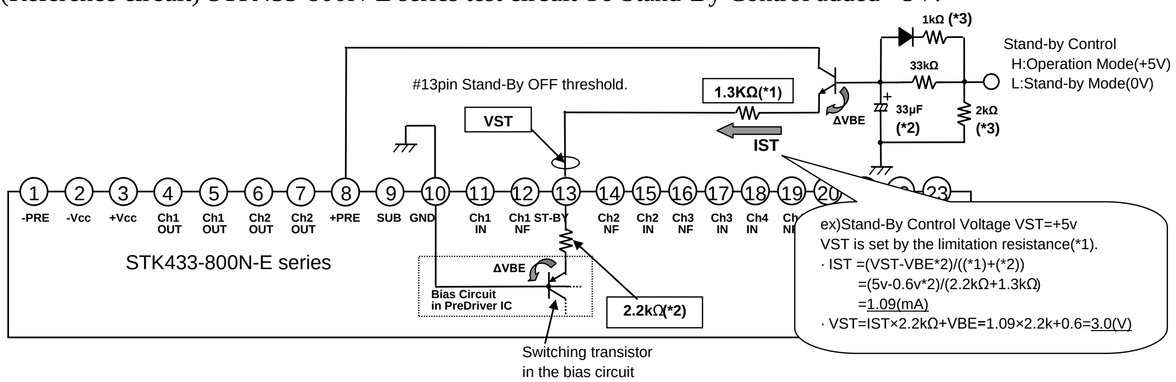


[STK433-800N-E series Stand-By Control Example]

[Feature]

- The pop noise which occurs to the time of power supply on/off can be improved substantially by recommendation Stand-By Control Application.
- Stand-By Control can be done by additionally adjusting the limitation resistance to the voltage such as micom, the set design is easy.

(Reference circuit) STK433-800N-E series test circuit To Stand-By Control added +5V.



[Operation explanation] #13pin Stand-By Control Voltage VST

(1) Operation Mode

The switching transistor in the bias circuit turns on and places the amplifier into the operating mode, when 13pin (VST) voltage added above 2.5V (typ 3.0V).

(2) Stand-By Mode

When 13pin (VST) voltage is stopped (= 0V), the switching transistor in the bias circuit turn off, placing the amplifier into the standby mode.

(*1) The current limiting resistor must be used to ensure that stand-by pin (13pin) voltage does not exceed its maximum rated value VST max.

(*2) The pop noise level when the power is turned on can be reduced by setting the time constant with a capacitor in operating mode.

(*3) Determines the time constant at which the capacitor (*2) is discharged in stand-by mode.

This catalog provides information as of April, 2012. Specifications and information herein are subject to change without notice.